

(19) World Intellectual Property Organization  
International Bureau



(43) International Publication Date  
9 October 2003 (09.10.2003)

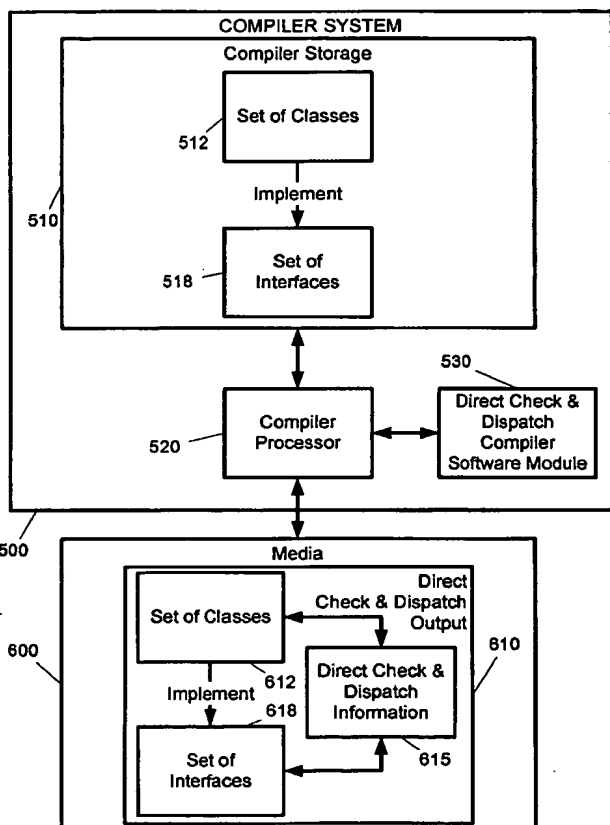
PCT

(10) International Publication Number  
WO 03/083653 A2

- (51) International Patent Classification<sup>7</sup>: **G06F 9/44**
- (21) International Application Number: PCT/CA03/00453
- (22) International Filing Date: 28 March 2003 (28.03.2003)
- (25) Filing Language: English
- (26) Publication Language: English
- (30) Priority Data:  
60/368,138 29 March 2002 (29.03.2002) US
- (71) Applicant (for all designated States except US): **RE-SEARCH IN MOTION LIMITED [CA/CA]**; 295 Phillip Street, Waterloo, Ontario N2L 3W8 (CA).
- (72) Inventors; and
- (75) Inventors/Applicants (for US only): **SCIEN, Anthony, F. [CA/CA]**; 402 Cavendish Drive, Waterloo, Ontario N2T 2N6 (CA). **YACH, David, P. [CA/CA]**; 254 Castlefield Avenue, Waterloo, Ontario N2K 2N1 (CA).
- (74) Agents: **PATHIYAL, Krishna, K. et al.**; Research In Motion Limited, 295 Phillip Street, Waterloo, Ontario N2L 3W8 (CA).
- (81) Designated States (*national*): AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NO, NZ, OM, PH, PL, PT, RO, RU, SC, SD, SE, SG, SK, SL, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, YU, ZA, ZM, ZW.
- (84) Designated States (*regional*): ARIPO patent (GH, GM, KE, LS, MW, MZ, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HU, IE, IT, LU, MC, NL, PT, RO, SE, SI, SK, TR), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

[Continued on next page]

(54) Title: SYSTEM AND METHOD FOR OBJECT ORIENTED INTERFACE CHECKING AND METHOD DISPATCHING



(57) Abstract: Method and system for interface checking and interface method dispatching for wireless devices. Relationships between classes and objectoriented interfaces are analyzed and certain properties are exploited for use in performing interface checking and/or interface method dispatching.

BEST AVAILABLE COPY

WO 03/083653 A2

**Declarations under Rule 4.17:**

- as to applicant's entitlement to apply for and be granted a patent (Rule 4.17(ii)) for the following designations AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NO, NZ, OM, PH, PL, PT, RO, RU, SC, SD, SE, SG, SK, SL, TJ, TM, TN, TR, TT, TZ, UA, UG, UZ, VC, VN, YU, ZA, ZM, ZW, ARIPO patent (GH, GM, KE, LS, MW, MZ, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HU, IE, IT, LU, MC, NL, PT, RO, SE, SI, SK, TR), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG)
- as to the applicant's entitlement to claim the priority of the earlier application (Rule 4.17(iii)) for the following designations AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DZ, EC,

EE, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NO, NZ, OM, PH, PL, PT, RO, RU, SC, SD, SE, SG, SK, SL, TJ, TM, TN, TR, TT, TZ, UA, UG, UZ, VC, VN, YU, ZA, ZM, ZW, ARIPO patent (GH, GM, KE, LS, MW, MZ, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HU, IE, IT, LU, MC, NL, PT, RO, SE, SI, SK, TR), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG)

- of inventorship (Rule 4.17(iv)) for US only

**Published:**

- without international search report and to be republished upon receipt of that report

For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.

**System and Method for Object Oriented  
Interface Checking and Method Dispatching**

**RELATED CASE**

This application claims the benefit of and priority to United States provisional patent application Serial No. 60/368,138, filed March 29, 2002, the entire disclosure of which is incorporated herein by reference.

5

**BACKGROUND**

1. **TECHNICAL FIELD**

The present invention relates generally to wireless devices, and more particularly to handling software objects operating on a wireless device.

10 2. **DESCRIPTION OF THE RELATED ART**

Fig. 1 illustrates a set 10 of object-oriented classes. Each class 15 is typically compiled from source code into object code. A class 15 typically defines attributes and implements methods. A class 15 may also provide a constructor method for constructing objects that are instances of that particular class, which are said to be objects of that class type. Classes 10 may be related to one another via a class hierarchy, wherein a subclass is related to a parent class whereby the subclass may inherit attributes and/or methods from the parent class.

Fig. 2 illustrates a set 20 of object-oriented interfaces. Each interface 20 25 is typically compiled from source code into object code. An interface 25

typically defines method signatures. As was the case with classes, interfaces  
20 may be related to one another via an interface hierarchy, wherein a sub-  
interface is related to a parent interface and whereby a sub-interface may  
inherit method signatures from a parent interface. However, interfaces 20  
5 differ from classes in at least two respects. First, interfaces do not implement  
methods, but rather define method signatures. Second, interfaces do not  
provide object constructors, but rather are used to cast objects of a particular  
class type into a particular interface type to enforce the exclusive use of the  
methods implemented in the class and accessible to the object, whose method  
10 signatures are defined in the interface.

### SUMMARY

In accordance with the teachings contained herein, a method and  
system are provided for interface checking and interface method dispatching  
15 on wireless devices. Relationships between classes and object-oriented  
interfaces are analyzed and certain properties are exploited for use in  
performing interface checking and/or interface method dispatching.

### BRIEF DESCRIPTION OF THE DRAWINGS

20 Fig. 1 illustrates a set of object-oriented classes;  
Fig. 2 illustrates a set of object-oriented interfaces;  
Fig. 3 illustrates the relationship between a typical class Cc and several  
interfaces;

Fig. 4 illustrates a class that implements two interfaces directly;

Fig. 5 illustrates a class that implements an interface indirectly via another interface;

Fig. 6 illustrates a class that implements an interface indirectly via the class hierarchy;

Fig. 7 illustrates an example set of classes and a set of interfaces;

Fig. 8 illustrates a flowchart of an example embodiment of the method;

Fig. 9 illustrates an exemplary embodiment of a method to carry out step 100 of Fig. 8;

Fig. 10 illustrates the first constraint of Fig. 9;

Fig. 11 illustrates a sample interface ordinal table;

Fig. 12 illustrates an exemplary technique for optimizing the NP-complete steps 110 and 130 of Fig. 10;

Fig. 13 illustrates a method to carry out step 120 of Fig. 9 to provide a class interface check (CIC) table;

Fig. 14 illustrates a sample CIC table, as can be provided by the method of Fig. 13;

Fig. 15 illustrates the second constraint of Fig. 9;

Fig. 16 illustrates a sample method ordinal table;

Fig. 17 illustrates a method of carrying out step 140 of Fig. 9 to provide a class interface dispatch (CID) table;

Fig. 18 illustrates a sample CID table, as can be provided by the method of Fig. 17;

Fig. 19 illustrates a method to carry out the step 200 of Fig. 8 to communicate direct check and dispatch information to a runtime device;

Fig. 20 illustrates a method to carry out the step 300 of Fig. 8 to perform a direct check and dispatch on a runtime device;

5        Fig. 21 illustrates an example embodiment of a direct interface check method;

Fig. 22 illustrates an example embodiment of a direct dispatch method;

Fig. 23 illustrates a direct check and dispatch system;

Fig. 24 illustrates in greater detail a compiler system and media of Fig.  
10    23;

Fig. 25 illustrates an example storage media of Figs. 23 and 24 in further detail;

Fig. 26 illustrates an example runtime device;

Fig. 27 illustrates in greater detail the exemplary runtime storage of  
15    Fig. 26;

Fig. 28 illustrates an embodiment of a method of compacting sparse CID tables in accordance with the optional step 800 of Fig. 17;

Fig. 29 illustrates sparse tables; and

Fig. 30 illustrates the same table information found in Fig. 29 but  
20    compacted by the method of Fig. 28.

#### DETAILED DESCRIPTION

Fig. 3 depicts relationships between a typical class Cc 15 and several interfaces 25. It was observed above that, in order for class Cc 15 to provide a constructor method for objects of type Cc, class Cc implements – either directly or indirectly – the methods whose signatures are defined in the  
5 interfaces 25 class Cc implements.

For the purposes of this description (and the appended claims), the term implements takes on a "deep" meaning as compared to the traditional "shallow" meaning, where "deep" and "shallow" refer to the traversal of the class and interface hierarchies to ascertain whether a class implements a  
10 method signature or an interface. However, it should be understood that certain situations may involve the term "implements" as having solely a "shallow" traversal while other situations may entail a wider meaning for the term "implements."

When considering the interfaces "shallowly" implemented by a class,  
15 only those interfaces explicitly declared as implemented by the class are considered. The concept of "shallow" implementation may be extended into a "deep" implementation, wherein the class and interface hierarchies are used to determine those method signatures and interfaces that are either directly (equivalent to "shallow") or indirectly implemented by the class.

20 The concept of direct vs. indirect implementation of method signatures and interfaces is described as follows. As used herein, a class implements directly any method signature or interface for which it explicitly declares and provides an implementation.

Furthermore, as used herein, a class implements indirectly via class hierarchy any method signature or interface it inherits from any of its super classes via the class hierarchy (i.e., from its parent class, or the parent class of its parent class, etc.; collectively known as the super classes of the class).

5        Further still, as used herein, a class implements indirectly via interface hierarchy any method signature or interface it inherits from any of its super interfaces via the class hierarchy (i.e., from its declared interfaces, from its super classes declared interfaces, or from any of the super interfaces of these declared interfaces).

10        In this description, whenever the term implements is used by itself, it will be understood that this is meant to include both direct and indirect implementation. Furthermore, whenever the term indirectly is used by itself, it will be understood that this is meant to include both indirectly via class hierarchy and indirectly via interface hierarchy.

15        Figs. 4-6 illustrate, by example, cases of direct and indirect implementation of interfaces by classes. In particular, three cases wherein a class implements an interface – either directly or indirectly – are illustrated. Rather than illustrating all possible combinations of direct and indirect implementation, the drawings illustrate the concept of considering the  
20        relationship between classes and interfaces, such as whether a class implements two interfaces simultaneously. As used in this description and in the appended claims, a class implements two interfaces simultaneously if the class can implement multiple interfaces and the class inherits, either directly or



indirectly, from each of the two interfaces. The method considers this type of relationship between interfaces and classes and is described in greater detail in reference to Figs. 8-22.

In the example of Fig. 4, class 15A implements two interfaces 25X,  
5 25Y directly.

In the example of Fig. 5, a class 15B implements an interface 25Y indirectly via another interface 25X. Class 15B, like class 15A in Fig. 4, implements directly interface 25X – as illustrated by the solid arrow connecting class 15B to interface 25X. However, unlike class 15A in Fig. 4,  
10 class 15B implements indirectly (via the interface hierarchy) interface 25Y. As illustrated, interface 25X is a sub-interface of 25Y, i.e., 25Y is either the parent interface of  $I_x$  or of any of the parent interfaces of 25Y in the interface hierarchy.

Finally, in the example of Fig. 6, class 15C implements an interface  
15 25X indirectly via the class hierarchy, i.e., via another class 15D.

With respect to Fig. 7, a specific set of classes 15P, 15R and a set of interfaces 25X, 25Y will be used, as an example, to illustrate the embodiments of the method and system illustrated in the remaining figures. As shown, class 15P implements interface 25Y directly. Furthermore, class 15R implements  
20 interface 25X directly. Class 15R is a subclass of 15P and thereby implements interface 25Y indirectly. Interface 25X contains method signature 26W, which is implemented by class 15R in method implementation 16W. Similarly,

interface 25Y contains method signature 26Z, which is implemented by class 15P in method implementation 16Z.

Also shown in Fig. 7, is example runtime object 35R, an instance of class 15R, which will be further described in reference to the runtime portions of the system and method, in Figs. 22 and 27.

With respect to the ternary relationship between class 15R and interfaces 25X and 25Y, class 15R implements simultaneously interfaces 25X and 25Y. In an assignment method, interface ordinal numbers 27X and 27Y are assigned to interfaces 25X and 25Y respectively. Furthermore, method ordinal numbers 28W and 28Z are assigned to method signatures 26M and 26Z respectively.

The ordinal assignments to the interfaces and interface methods are stored in direct check and dispatch information data structures. During operation of a runtime device, the device may use the data structures to directly check if a runtime object can be cast into a particular type of interface. If so, then the device may further use the data structures to facilitate a direct dispatch of the desired method(s) associated with the checked interface. Generation of check and dispatch information is described in greater detail in reference to Figs. 8-22.

With reference to Fig. 8, a flowchart of one embodiment of the method, by way of example, is illustrated. Three steps 100, 200, and 300 are illustrated.

In the first step 100, check and dispatch information is generated on a compiler system. This step is described in greater detail with reference to Figs. 9-18. Furthermore, an exemplary embodiment of the compiler system is described in further detail with reference to Fig. 23 wherein the compiler system is illustrated as a part of a larger system, and with reference to Fig. 24 wherein an exemplary compiler system is illustrated. In an alternative embodiment, it is contemplated that this step may be carried out on the runtime device.

In the second step 200, the check and dispatch information generated in step 100 is communicated to a runtime device. This step is described in greater detail in reference to Fig. 19. Check and dispatch information may be prepared, stored, and communicated to a runtime device, via media which is described with reference to Fig. 23 wherein the media is illustrated as a part of a larger system, and with reference to Fig. 25 wherein an exemplary media is illustrated. In an alternative embodiment, it is contemplated that this step may be carried out on the runtime device.

In the third and final step 300, the check and dispatch information generated in step 100, and communicated in step 200, is used to perform, first a direct check step and then a direct dispatch step on a runtime device. This step is further illustrated in greater detail in Fig. 20, wherein direct check and direct dispatch steps are also illustrated. The direct check step is further detailed with reference to Fig. 21, whereas the direct dispatch step is further detailed with reference to Fig. 22. The runtime device is illustrated as a part

of a larger system in Fig. 23, and an exemplary runtime device, a wireless device, is illustrated in greater detail in Figs. 26 and 27.

Although not expressly shown in the drawings, some portions of step 100 can be carried out on the runtime device. In the case where all portions of  
5 step 100 are carried out on the runtime device, then step 200 may need not be carried out as the runtime device and compiler system can be coterminous.

With reference to Fig. 9, an exemplary embodiment of a method to carry out step 100 of Fig. 8 is illustrated.

At step 110, interface ordinals are assigned to interfaces, while  
10 optimizing a first constraint. The constraint is illustrated by the expression 115 of Fig. 10, which states that interface ordinals are to be assigned to enforce the rule that two interface ordinals (such as for example with reference to Fig. 7 interface ordinal 27X and ordinal 27Y) are different if there exists a class in the system that directly or indirectly implements both interfaces at the  
15 same time, such as for example with reference to Fig. 7 class 15R. At the end of step 110,  $\max_i$  interface ordinals will have been assigned to the interfaces of the system.

A sample interface ordinal table 117 is illustrated in Fig. 11. Note that more than one interface may share the same interface ordinal in order to  
20 minimize the value of  $\max_i$ , the number of interface ordinals. In the sample interface ordinal table 117 of Fig. 11, additional entries are shown to illustrate the cases where many more classes and interfaces are in the system, not only those shown in the example of Fig. 7. Step 110 can be accomplished using any

number of techniques for optimizing NP-complete problems. One such technique 300 is illustrated in Fig. 12, which will be described next.

With reference to Fig. 12, a constraint is provided at step 310, and at step 320 a set of items to be assigned ordinals is also provided. Items are  
5 assigned the same ordinal 0 at step 330. At step 340, the items are tested for satisfying the constraint. If the constraint is not satisfied (such as due to at least two items not satisfying the constraint and thereby resulting in a "collision" of their ordinal assignments), step 350 resolves this by using any one of a number of known techniques such as randomly choosing or using a  
10 weighted method. For example, by randomly choosing one of the interfaces involved in a conflict to have its ordinal changed (e.g., by adding a value of one to it) the conflict is thus resolved. Processing returns to step 340.

If the constraint is satisfied as determined at step 340, then step 360 ensues. At step 360, the ordinal assignments and the maximum number of  
15 ordinals in the system are stored.

With reference back to Fig. 9, having described step 110, the next step 120 of creating class interface check (CIC) tables is described next in greater detail with reference to Fig. 13. At step 121, all classes in the system are processed in turn through steps 122 through 127. At step 122, a CIC table is  
20 created with  $\max_i$  null references for the class  $C_c$  currently being processed. At step 123, all interfaces in the system are processed in turn through steps 125 through 126. At step 125, the class  $C_c$  currently being processed is tested for directly or indirectly implementing the interface  $I_i$  currently being

processed. If class  $C_c$  implements interface  $I_i$ , then at step 125, the reference corresponding to the interface ordinal of interface  $I_i$  in the CIC table of class  $C_c$  is set to point to the interface  $I_i$ . At step 126, if all interfaces have been processed, then step 127 ensues, if not then the next interface to process is  
5 selected, and processing continues at step 124. At step 127, if all classes have been processed, then the method ends, if not then the next class to process is selected, and processing continues at step 122.

A sample CIC table 128, as can be provided by the method of Fig. 13, is illustrated in Fig. 14. Note that optionally only entries that correspond to  
10 interface ordinals of methods that are implemented by the particular class have non-null entries. For example with reference to Fig. 7, the CIC table corresponding to class  $C_r$  would have non-null entries at ordinals 27X and 27Y corresponding to interfaces 25X and 25Y respectively. In the sample CIC table 128 of Fig. 14, additional entries are shown to illustrate the cases where  
15 many more classes and interfaces are in the system, not only those shown in the example of Fig. 7.

With reference back to Fig. 9, having described step 120, the next step  
130 of assigning method ordinals to interface method signatures, while optimizing a second constraint, is described next in greater detail with  
20 reference to Figs. 15, 16 and Fig. 12. The constraint is illustrated by the expression 135 of Fig. 15, which states that method ordinals are to be assigned to enforce the rule that two method ordinals (such as for example with reference to Fig. 7 method ordinal 28W and ordinal 28Z) are different if there

exists a class in the system that directly or indirectly implements both methods at the same time, such as for example with reference to Fig. 7 class 15R. At the end of step 130,  $\max_m$  method ordinals will have been assigned to the interface method signatures of the system.

5           A sample method ordinal table 137 is illustrated in Fig. 16. It is noted that more than one method may share the same method ordinal in order to minimize the value of  $\max_m$ , the number of method ordinals. In the sample method ordinal table 135 of Fig. 16, additional entries are shown to illustrate the cases where many more classes and interfaces are in the system, not only  
10 those shown in the example of Fig. 7. As was the case with step 110, step 130 can be accomplished using any number of techniques for optimizing NP-complete problems. One such technique 300 is illustrated in Fig. 12, which was already described above.

With reference back to Fig. 9, having described step 130, the next step  
15 140 of creating class interface dispatch (CID) tables is described next in greater detail with reference to Fig. 17. At step 141, all classes in the system are processed in turn through steps 142 through 147. At step 142, a CID table is created with  $\max_m$  null references for the class  $C_c$  currently being processed. At step 143, all interfaces in the system are processed in turn  
20 through steps 145 through 146. At step 145, the class  $C_c$  currently being processed is tested for directly or indirectly implementing the interface  $I_i$  currently being processed, and may use the CIC table. If class  $C_c$  implements interface  $I_i$ , then at step 145, the reference corresponding to the method ordinal

of each method signature in interface  $I_i$  in the CID table of class  $C_c$  is set to point to the implementation of the method signature of Class  $C_c$ . This step may involve the use of the virtual function table for class  $C_c$  and a deep traversal of the class hierarchy of class  $C_c$ . At step 146, if all interfaces have  
5 been processed, then step 147 ensues, if not then the next interface to process is selected, and processing continues at step 144. At step 147, if all classes have been processed, then the method ends, if not then the next class to process is selected, and processing continues at step 142. After all classes have been processed, step 800 may follow in order to compact the tables. This step  
10 800 is an optional improvement. A method of carrying out step 800 is discussed in greater detail with reference to Figs. 28-30.

A sample CID table 148, as can be provided by the method of Fig. 17, is illustrated in Fig. 18. Note that optionally only entries that correspond to method ordinals of methods that are implemented by the particular class have  
15 non-null entries. For example with reference to Fig. 7, the CID table corresponding to class  $C_r$  would have non-null entries at ordinals 28W and 28Z corresponding to method signatures 26W and 26Z respectively. In the sample CID table 148 of Fig. 18, additional entries are shown to illustrate the cases where many more classes and interfaces are in the system, not only those  
20 shown in the example of Fig. 7.

With reference back to Fig. 8, having described step 100, the next step 200 of communicating direct check and dispatch information to a runtime device is described next in greater detail with reference to Fig. 19.



With reference to Fig. 19, at step 210 direct check and dispatch information is provided, which was generated at step 100. Direct check information includes interface ordinal assignments and CIC tables. Direct dispatch information includes method ordinal assignments and CID tables.

- 5 Direct check and dispatch information may be provided by itself, or optionally bundled with auxiliary information such as the set of classes and interfaces, as part of a larger direct check and dispatch output.

At step 220, it is determined whether the compiler system that provided the direct check and dispatch information at step 210, is physically  
10 connected to a runtime device. An example of physical connection is if the runtime device is electrically connected to the compiler system, such as for instance sitting in a cradle connected to the compiler system via a serial interface or fiber. An example of a non-physical connection is envisaged when the runtime device is a wireless communication device.

- 15 At step 230, if the runtime device was found to be connected physically to the compiler system at step 220, the direct check and dispatch information is communicated to the runtime device by the compiler system over the physical connection.

At step 240, if the runtime device was not found to be connected  
20 physically to the compiler system at step 220, the direct check and dispatch information is communicated to the runtime device wirelessly, if the runtime device is a wireless device.

With reference back to Fig. 8, having described step 200, the next step 300 of performing direct check and dispatch on a runtime device is described next in greater detail with reference to Fig. 20.

With reference to Fig. 20, at step 310 a runtime object  $O_r$  is provided, such as for example, with reference to the example of Fig. 7, runtime object 35R.

At step 320, an interface  $I_r$  is provided, such as for example, with reference to the example of Fig. 7, interface 25Y.

At step 330, a direct interface check is performed to verify if runtime object  $O_r$  can be cast into the type of interface  $I_r$ . An example embodiment of a method to carry out this step is illustrated in Fig. 21, and will be described immediately after Fig. 20. If the object  $O_r$  passes the check, then steps 340 and 350 ensue.

At step 340, a method signature  $m_m$  is provided, selected from the method signatures found in interface  $I_r$  for the purpose of calling the implementation of method  $m_m$  provided via object  $O_r$ .

At step 350, a direct dispatch is performed of method  $m_m$  via interface  $I_r$  of object  $O_r$ . An example embodiment of a method to carry out this step is illustrated in Fig. 22, and will be described after Fig. 21.

However, if the object  $O_r$  does not pass the check at step 330, then at step 360 a runtime exception is thrown.

With reference to Fig. 21, an example embodiment of a direct interface check method is described next. At step 331, the class data  $C_r$  for object  $O_r$  is

provided. This class data allows the runtime system to determine what class type provided the constructor for the Or object.

At step 332, knowing the class Cr of object Or, the CIC(Cr) table for Cr found in the direct check and dispatch information, is provided.

5       At step 333, the interface ordinal ordinali(I<sub>i</sub>) of interface I<sub>i</sub> is provided, for example by looking it up in the interface ordinal table of the direct check and dispatch information.

At step 334, the entry in the CIC(Cr) table corresponding to the interface ordinal ordinali(I<sub>i</sub>) of interface I<sub>i</sub> is compared to a reference to  
10   interface I<sub>i</sub> to perform the direct check. If the reference is equal to I<sub>i</sub>, then Object Or can be cast into the type corresponding to interface I<sub>i</sub>, and step 335 ensues wherein object Or is asserted as an instance of interface I<sub>i</sub>. However, if the reference is not equal to I<sub>i</sub>, then object Or cannot be cast into the type corresponding to interface I<sub>i</sub>, and wherein object Or is asserted as not being an  
15   instance of interface I<sub>i</sub>.

With reference back to Fig. 20, having described step 330 performing a direct check with reference to Fig. 21, the step 350 of performing direct dispatch is described next in greater detail with reference to Fig. 22.

With reference to Fig. 22, an example embodiment of a direct dispatch  
20   method is described next. At step 351, the class data Cr for object Or is provided. This class data allows the runtime system to determine what class type provided the constructor for the Or object.

At step 352, knowing the class Cr of object Or, the CID(Cr) table for Cr found in the direct check and dispatch information, is provided.

At step 353, the method ordinal ordinal<sub>m</sub>(m<sub>m</sub>) of interface method m<sub>m</sub> is provided, for example by looking it up in the method ordinal table of the  
5 direct check and dispatch information.

At step 355, method m<sub>m</sub> is directly dispatched.

Having described the three steps of the method of Fig. 8, a system suitable to cooperate with an embodiment of the method will be described next with reference to Figs. 23-27.

10 With reference to Fig. 23, a direct check and dispatch system 400 is illustrated. The direct check and dispatch system 400 includes a compiler system 500, media 600, and a runtime system 700. The compiler system 500 generates the direct check and dispatch information. The media 600 is used to prepare or store the direct check and dispatch information for communication  
15 to the runtime device 700. The runtime device 700 performs the direct check and dispatch using the direct check and dispatch information. Examples of each of these components of system 400 will be described next in reference to Figs. 24-27.

20 With reference to Fig. 24, the compiler system 500 and media 600 of Fig. 23 are illustrated in greater detail.

The compiler system 500 may include compiler storage 510 wherein can be stored a representation of a set of classes 512 and a representation of a set of interfaces 518. The representations 512, 518 may be in source code

form, object form, a reference to a network source, or any other representation that allows processor 520 to provide the set of interfaces and classes to the direct check and dispatch compiler software module 530. The direct check and dispatch compiler software module 530 may embody the method steps  
5 100 and 200 of Fig. 8.

In some situations, the direct check and dispatch compiler software module 530 generates CIC and CID tables wherein interface and method ordinals are respectively assigned to interfaces and interface methods such that a first and second constraint are substantially satisfied. The constraints are  
10 used along with a technique for optimizing NP-complete ordinal assignment problems (as described above) during the generation of the direct check and dispatch information. The check and dispatch information is later used by a runtime device, such as for performing a direct interface check method wherein the CIC table is directly consulted using the interface ordinal to  
15 perform the check. The runtime device can also perform a direct dispatch method wherein the CID table is directly consulted using the method ordinal to perform the dispatch.

The generated check and dispatch information 615 may be stored on media 600. Although not necessary, the media 600 may also include as part of  
20 the direct check and dispatch output 610 not only the direct check and dispatch information 615, but optionally a representation of the corresponding set of classes 612 and a representation of the set of interfaces 618. The representations 612 and 618 need not be the same as the corresponding

representations 512 and 518 found in the compiler system. The media 600 itself is intended to encompass both storage devices, such as RAM, flash, and disks, and communication media such as network connections and data streams.

5           With reference to Fig. 25, an exemplary storage media 600, such as flash, is illustrated. For a logical view of some of the same elements, see Fig. 7. Direct check and dispatch output 610 includes a set of classes 612, a set of interfaces 618, and direct check and dispatch information 615. The set of classes illustrated includes Cr class data 15R and other class data 15P (see Fig. 10 7 for a logical view). The set of interfaces 618 includes  $I_x$  25X interface data, and 25Y interface data. Direct check and dispatch information 615 includes CIC tables 616 and CID tables 617, as well as interface and method ordinals 27X, 27Y and 28W, 28Z respectively. Only one CIC table  $CIC(Cr)$  and one CID table  $CID(Cr)$ , both related to class Cr 15R, are illustrated.  $CIC(Cr)$  table 15 616 has non-null entries at interface ordinal positions  $ord_i(I_x)$  and  $ord_i(I_y)$  corresponding to interface ordinals of interfaces  $I_x$  25X and  $I_y$  25Y respectively.  $CID(Cr)$  table 617 has non-null entries at method ordinal positions  $ord_m(m_w)$  and  $ord_m(m_z)$  corresponding to method ordinals of interface methods  $m_w$  and  $m_z$  of interfaces  $I_y$  25Y and  $I_x$  25X respectively.

20           With reference to Fig. 26, an exemplary runtime device, which happens to be a wireless device 700, is illustrated. Runtime device 700 includes runtime storage 705 to receive and store a representation of direct check and dispatch information 715 via media 600. The direct check and

dispatch information 715 need not be in the same representation 615 as found in media 600, or as found in compiler system 500. As illustrated, the direct check and dispatch information 715 is part of direct check and dispatch input 710, which optionally includes representations 712 and 718 of the set of classes and interfaces, respectively. In addition to the direct check and dispatch information, runtime storage also includes a runtime context 750, which is used in conjunction with the direct check and dispatch information 615. The runtime context 750 may include any particular objects for which the direct check is performed, as well as any particular method calls on objects for which the direct dispatch is performed.

Runtime processor 720 may execute the direct check and dispatch runtime software module 730 and provides direct check and dispatch information 715 to module 730 for the purpose of performing the direct check and dispatch method. It should be understood that the runtime software module may be implemented as a single program or a set of programs (e.g., a module to perform the direct check and another module to perform the direct dispatch). Runtime software module 730 may embody the step 300 of the method of Fig. 8.

Also shown is transceiver 740 that allows runtime device 700 to communicate with compiler system 500 over wireless media 600.

With reference to Fig. 27, exemplary runtime storage 705 is illustrated. See Fig. 7 for a logical view of some of the same elements. Direct check and dispatch output 710 includes a set of classes 712, a set of interfaces 718, and

direct check and dispatch information 715. The set of classes illustrated includes Cr class data 15R and other class data 15P (see Fig. 7 for a logical view). The set of interfaces 718 includes  $I_x$  25X interface data and 25Y interface data. Direct check and dispatch information 715 includes CIC tables 716 and CID tables 717, as well as interface and method ordinals 27X, 27Y and 28W, 28Z respectively. Only one CIC table  $CIC(Cr)$  and one CID table  $CID(Cr)$ , both related to class Cr 15R, are illustrated.  $CIC(Cr)$  table 716 has non-null entries at interface ordinal positions  $ord_i(I_x)$  and  $ord_i(I_y)$  corresponding to interface ordinals of interfaces  $I_x$  25X and  $I_y$  25Y respectively.  $CID(Cr)$  table 717 has non-null entries at method ordinal positions  $ord_m(m_w)$  and  $ord_m(m_z)$  corresponding to method ordinals of interface methods  $m_w$  and  $m_z$  of interfaces  $I_y$  25Y and  $I_x$  25X respectively. Also illustrated is runtime context 750 which includes Or object data 35R (see Fig. 7) and an Or method call 770. The Or method call 770 is illustrated at the position of the program counter PC for the runtime context 750 as this condition could be used as a trigger to invoke the direct check and dispatch method of Fig. 20 wherein at step 340 a runtime method call is provided. Also shown is virtual function table 760 which includes the addresses of the two interface methods of the example of Fig. 7,  $m_z(Cr)$  and  $m_w(Cr)$  each specified by interfaces 25Y and 25X respectively, and implemented by class data 15P and 15R respectively.

A method of compacting sparse CID tables in accordance with the optional step 800 of Fig. 17, will be described next with reference to Fig. 28.



After which, in order to better illustrate this method, Figs. 29 and 30 will be described. Fig. 29 illustrates sparse tables. Fig. 30 illustrates the same table information found in Fig. 29 compacted by the method of Fig. 28.

With reference to Fig. 28, at step 810 a Compact Table is allocated and  
5 all of its entries are set to null. The Compact Table can be allocated to accommodate as many entries as there are in all of the candidate sparse CID tables. For instance, if each of 3 candidate CID tables has 9 entries each corresponding to 9 ordinals (0 to 8), then the Compact Table can be allocated to have 27 (3x9) entries. The method will actually use less memory than was  
10 allocated, therefore step 890 is provided to recover unused memory, both in the Compact Table and in the CID tables, at the end of the method.

At step 820, each candidate sparse CID table is processed in turn through steps 830 to 870.

At step 830, an offset value is set to zero. The offset value indicates  
15 how many entries of the Compact Table should be skipped in order to find the first entry of the sparse CID table being processed at step 820.

At step 840, all entries of the candidate sparse CID table are compared to the corresponding entries in the Compact Table in order to determine if the candidate sparse CID table would fit at the current offset. For a given offset,  
20 correspondence between candidate sparse CID table entries and Compact Table entries is done by adding the ordinal number of the CID table entry to the offset of the Compact Table. A candidate sparse CID table fits at a given offset if and only if all non-null entries at ordinal position in the candidate

sparse CID table find correspondence with null entries at position [offset + ordinal] in the Compact Table. If the candidate sparse table fits, then steps 860 and 870 ensue, or else step 850 ensues wherein the offset is incremented, and step 840 is performed again. Eventually, the offset will be sufficiently large so  
5 that step 840 determines that the candidate sparse table fits.

At step 860, the non-null entries in the candidate sparse CID table are copied to their corresponding position in the Compact Table.

At step 870, the address of the offset in the Compact Table is used as the new origin of the candidate sparse CID table, for example by setting the  
10 handle in memory which previously pointed to the address of the first entry in the candidate sparse CID table to point to the address of the offset entry in the Compact Table. Once step 870 is complete, the memory that was at the old candidate sparse CID table can be recovered or marked for recovery.

At step 880, the next candidate sparse CID table is processed through  
15 steps 830 to 870, or having processed all sparse CID tables, the method continues at step 890.

At step 890, memory that remains unused can be recovered. For instance, the memory of each candidate sparse CID table marked for recovery at step 870 can be recovered. Furthermore, all null entries at the end of the  
20 Compact Table can be recovered.

With reference to Figs. 29 and 30, the equivalence of the compact representation of Fig. 30 with the sparse representation of Fig. 29 will be described with reference to the steps of the method at Figs. 20-22.

With reference to Fig. 29, three sparse CID tables are shown: 920X, 920Y and 920Z that collectively form the sparse table input 910 for the method of Fig. 28. A sparse CID table has non-null entries, and being sparse, null entries. Table 920X has three non-null entries  $X_0$ ,  $X_4$  and  $X_8$  at ordinal positions 0, 4 and 8 respectively. Table 920Y has two non-null entries  $Y_0$  and  $Y_5$  at ordinal positions 0 and 5 respectively. Table 920Z has two non-null entries  $Z_2$  and  $Z_4$  at ordinal positions 2 and 4 respectively. Each CID table 920X, 920Y, and 920Z has a handle 925X, 925Y and 925Z respectively which points to the address of the first entry of the respective CID table. The sparse table input 910 resides in either storage or media 900.

With reference to Fig. 30, the same sparse CID table information is shown, i.e., every non-null entry of the sparse table input 910 of Fig. 29 can be found in the compact tables output 930 of Fig. 30. Furthermore, so long as only non-null entries are accessed using the updated handles 925X, 925Z and 925Y, then compact table 940 provides a compact representation of the sparse CID tables of Fig. 29.

With reference back to Figs. 20-22, step 353 of Fig. 22 could equally well use the tables of Fig. 29 or Fig. 30 to provide non-null entry  $\text{ordinal}_m(m_m)$ . This is because the direct check step 334 of Fig. 21 prevents the direct dispatch step 353 of Fig. 22 from accessing non-null CID table entries by throwing an exception at step 336.

Having described in detail the preferred embodiments of the present invention, including the preferred methods of operation, it is to be understood

that this operation could be carried out with different elements and steps. This preferred embodiment is presented only by way of example and is not meant to limit the scope of the present invention. As an illustration, the methods and systems disclosed herein are well suited for use in many wireless devices, such as personal digital assistants, mobile communication devices, cellular phones, and wireless two-way communication devices. Moreover, the methods and systems disclosed herein are useful in any device that uses an object-oriented computer system as well as can be implemented via compiler systems, computer-readable media, and runtime devices. The systems and methods may also have their information stored in data structures which are contained in memory as well as be transmitted via data signals embodied on carrier signals or other communication pathway media (e.g., fiber optics, infrared, etc.).

It is claimed:

1. A method for handling runtime objects operating on a wireless device,  
wherein the runtime objects are instantiations of classes, wherein a class can  
5 implement an object-oriented interface having methods, comprising the steps  
of:

assigning, for a class, an interface ordinal to an interface based  
upon a relationship existing between the class and the interface;

associating with at least one interface ordinal a reference that  
10 points to an interface implemented by a class;

assigning, for a class, a method ordinal to an interface's' method  
based upon a relationship existing between the class and the method;

associating with at least one method ordinal a reference that  
points to a method of an interface;

15 wherein an assigned interface ordinal and an associated  
reference are to be used to check whether a runtime object operating on the  
wireless device is an instantiation of an interface;

wherein an assigned method ordinal and an associated method  
reference are to be used to perform a dispatch of an interface's method  
20 implemented by the runtime object.

2. The method of claim 1, wherein a direct interface check is performed to verify if the runtime object can be cast into the type of an interface by performing steps comprising:

- providing class data for the runtime object in order to determine  
5 what class type provided the constructor for the runtime object;  
using the interface ordinals' association with the interface references to check whether the runtime object is an instance of an interface.

3. The method of claim 1, wherein a dispatch is performed by performing  
10 steps comprising:

- providing class data for the runtime object in order to determine  
what class type provided the constructor for the runtime object;  
using the method ordinals association with the method references to directly dispatch the method.

15

4. The method of claim 1, wherein a compiler system generates check and dispatch information based upon an input set of classes and set of interfaces,  
wherein the check and dispatch information includes the interface ordinals, the interface references associated with the interface  
20 ordinals, the method ordinals, and the method references associated with the method ordinals, wherein the wireless device includes runtime storage to store on the wireless device the assigned interface ordinals and their associated interface references, .

5. The method of claim 4, wherein the runtime storage stores on the wireless device the assigned method ordinals and their associated method references.

5 6. The method of claim 5, wherein the runtime storage comprises runtime context which includes objects for which a direct check is performed and includes method calls on objects for which a direct dispatch is performed.

7. The method of claim 6, wherein the wireless device includes a runtime processor and operates within a Java-based computer environment, wherein an assigned interface ordinal and an associated reference are to be used by the runtime processor to check whether a runtime object operating on the wireless device is an instantiation of an interface;

wherein an assigned method ordinal and an associated method  
15 reference are to be used by the runtime processor to perform a dispatch of an  
interface's method implemented by the runtime object.

8. The method of claim 7, wherein a method call on the wireless device is used as a trigger to invoke a direct check and direct dispatch using the check and dispatch information.

9. A system for handling runtime objects operating on a wireless device, wherein the runtime objects are instantiations of classes, wherein a class can implement an object-oriented interface having methods, comprising:

class interface check data structure, wherein the class interface  
5 check data structure stores associations between interface ordinals and references to interfaces,

wherein the ordinals were assigned to interfaces such that a first constraint is substantially satisfied, wherein the first constraint provides that two interfaces are assigned different interface ordinals if there exists a class  
10 that implements both of the interfaces;

class interface dispatch data structure, wherein the class interface dispatch data structure stores associations between method ordinals and references to methods,

wherein the ordinals were assigned to methods such that a  
15 second constraint was substantially satisfied, wherein the second constraint provides that two methods have different method ordinals if there exists a class that implements both of the methods;

wherein the class interface check data structure is to be used to check whether a runtime object operating on the wireless device is an  
20 instantiation of an interface;

wherein the class interface dispatch data structure is to be used to perform a dispatch of an interface's method implemented by the runtime object.



10. The system of claim 9 further comprising:

a compiler system that assigns the ordinals to interfaces such  
that the first constraint is substantially satisfied, wherein the compiler assigns  
5 the ordinals to methods such that the second constraint is substantially  
satisfied.

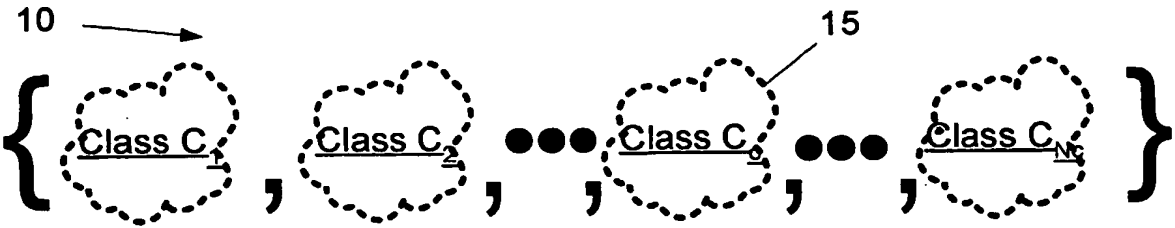


FIG. 1

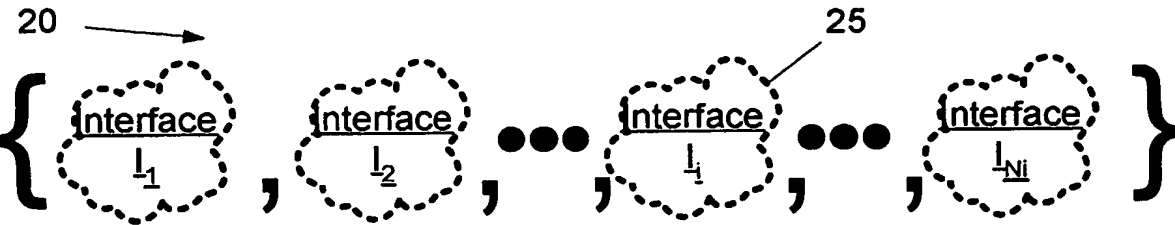


FIG. 2

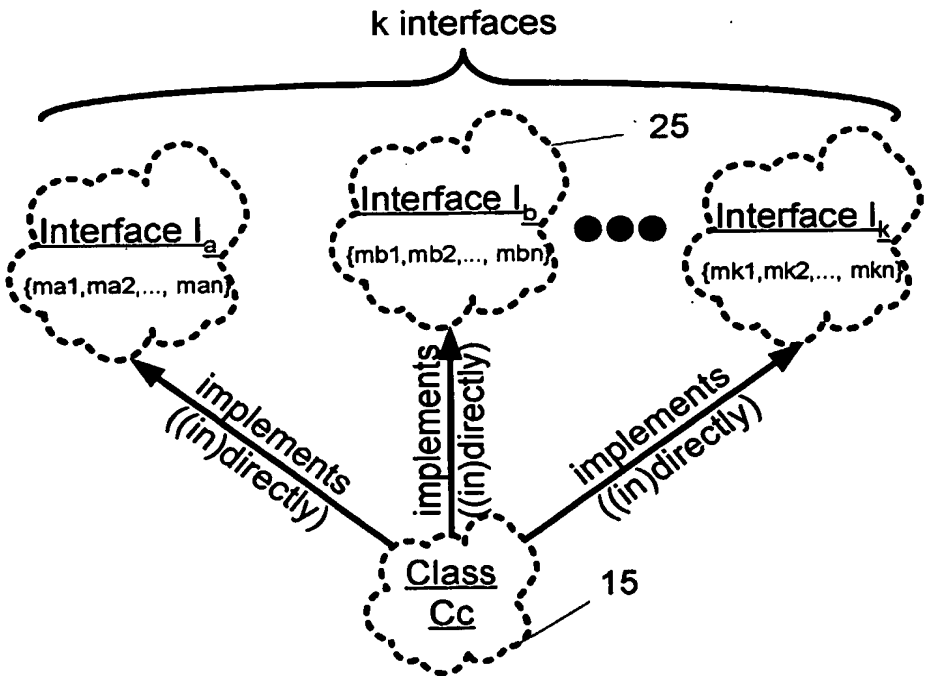


FIG. 3

2/25

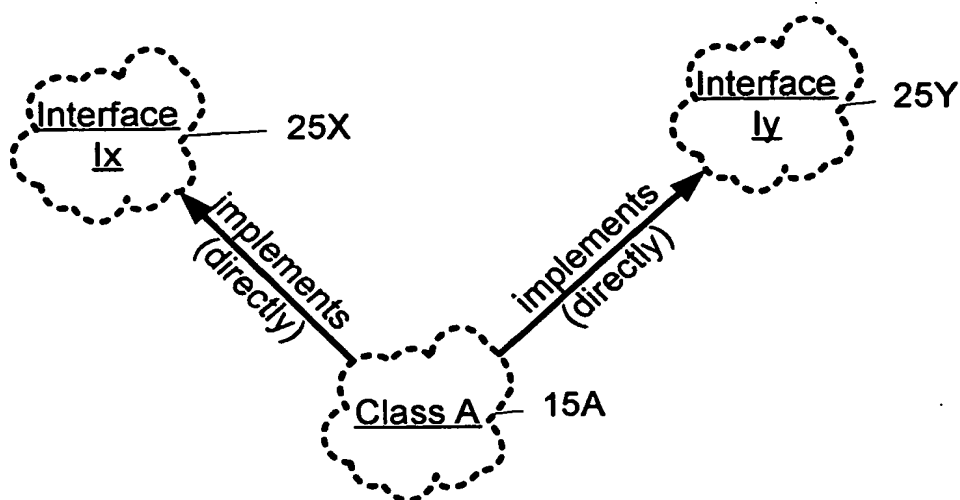


FIG. 4

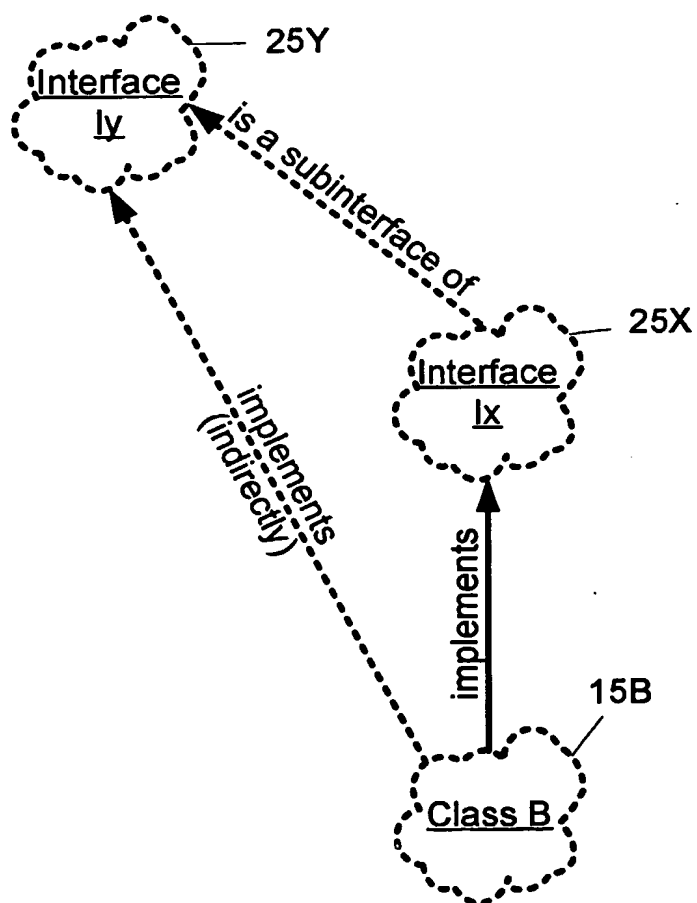


FIG. 5

3/25

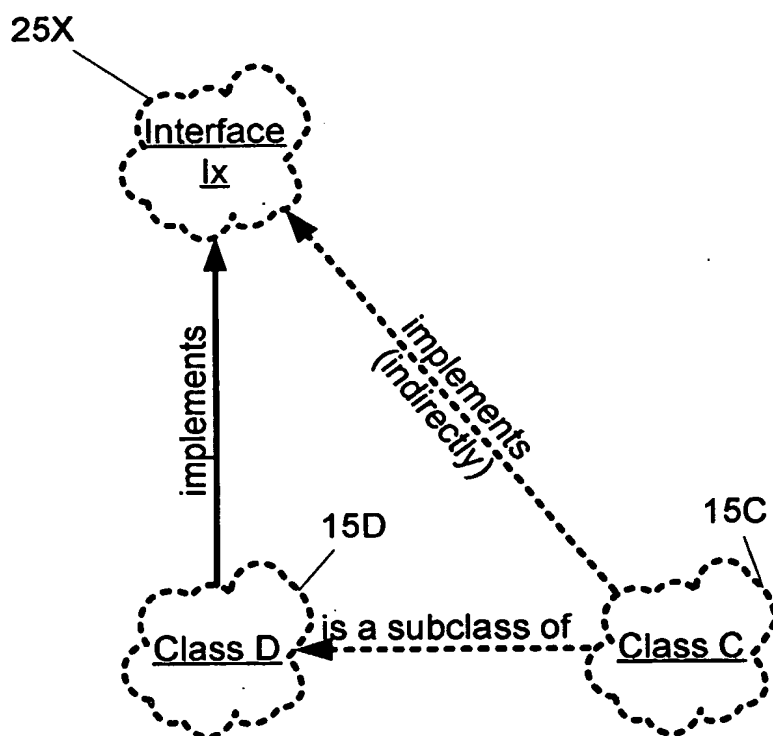


FIG. 6

4/25

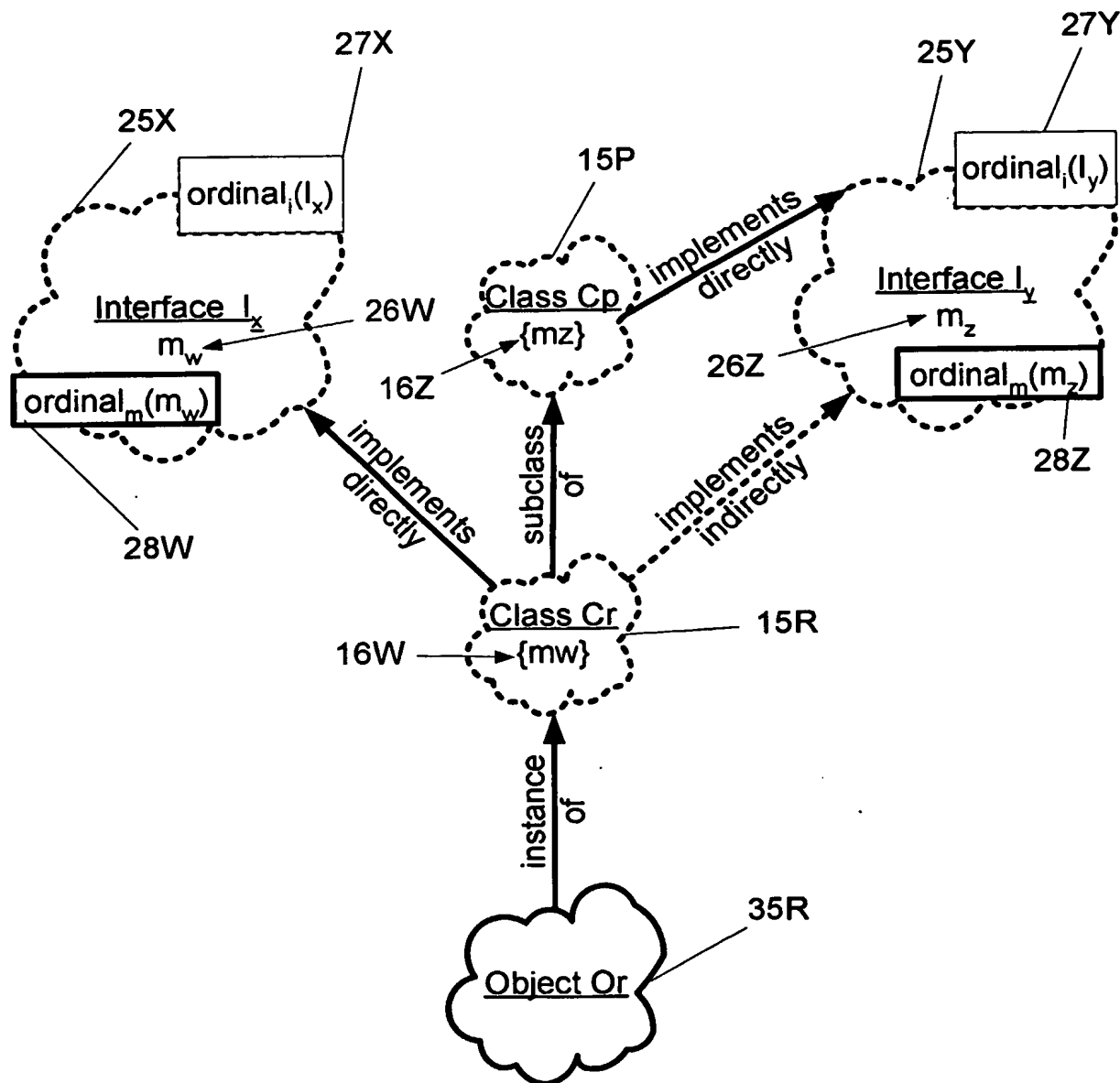


FIG. 7

5/25

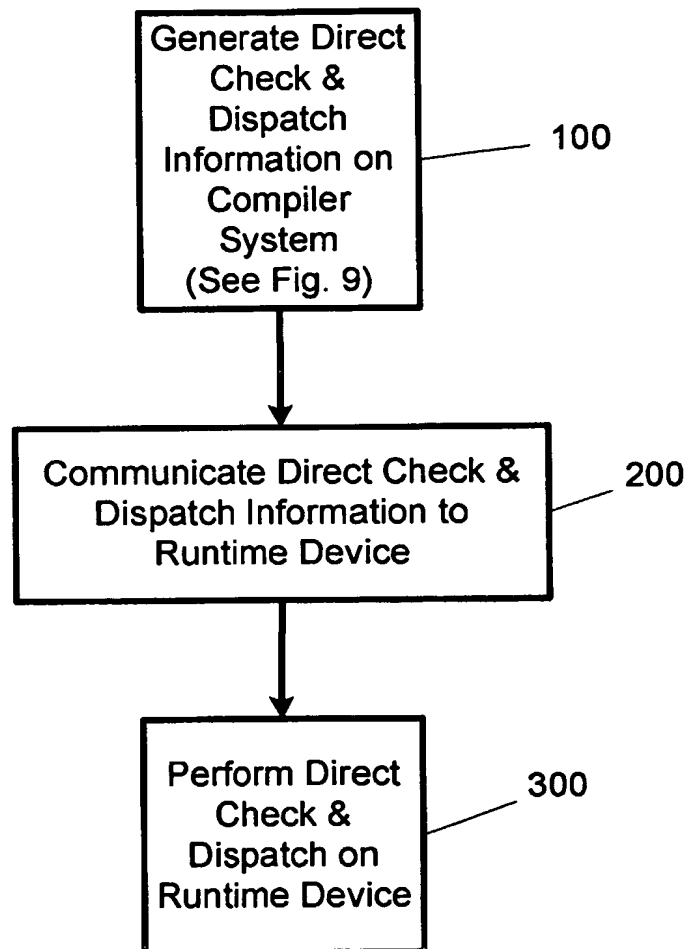


FIG. 8

6/25

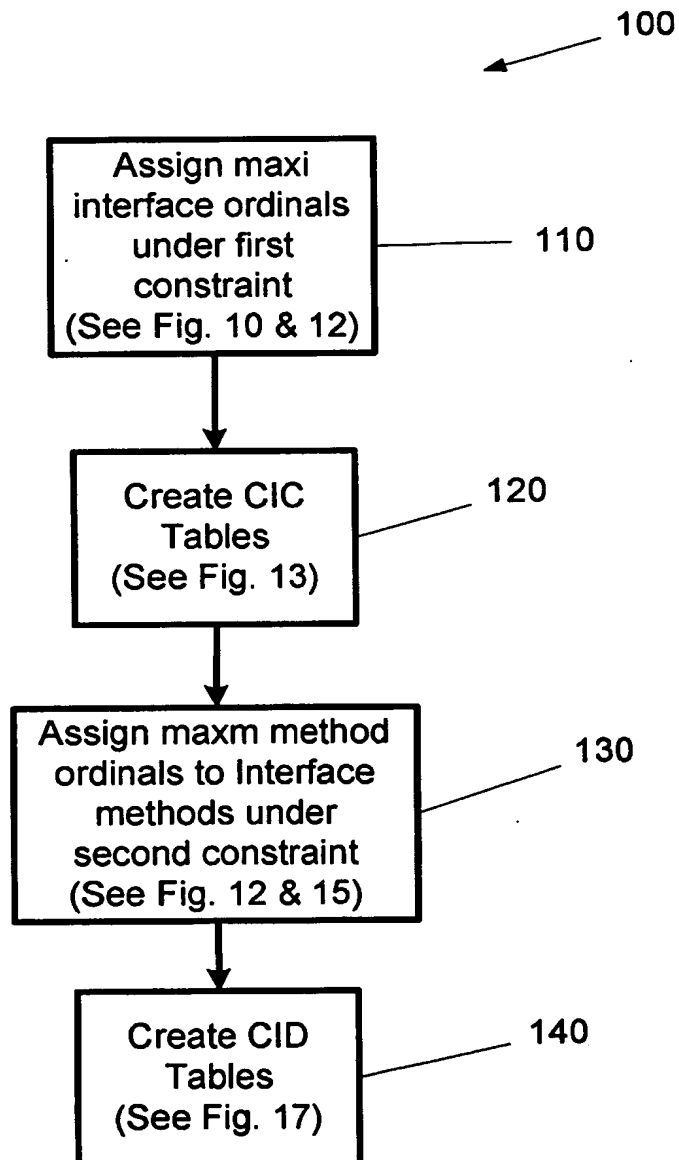


FIG. 9

7/25

FIRST CONSTRAINT

$$\left\{ \begin{array}{l} \forall I_x, I_y, C_r | \\ C_r \text{ implements } I_x \wedge C_r \text{ implements } I_y \\ \Rightarrow \\ (\text{ordinal}_i(I_x) \diamond \text{ordinal}_i(I_y)) \\ \wedge \\ (\text{ordinal}_i(I_x), \text{ordinal}_i(I_y) \in \{0, 1, \dots, \text{max}_i\}) \end{array} \right\}$$

115

FIG. 10

117

Interface ordinals	
ordinal <sub>i</sub>	Interfaces
0	{l <sub>a0</sub> , l <sub>b0</sub> , l <sub>c0</sub> , ..., l <sub>d0</sub> }
1	{l <sub>a1</sub> , l <sub>b1</sub> , l <sub>c1</sub> , ..., l <sub>d1</sub> }
...	...
ordinal <sub>i</sub> (l <sub>x</sub> )	l <sub>x</sub>
...	...
ordinal <sub>i</sub> (l <sub>y</sub> )	l <sub>y</sub>
...	...
max <sub>i</sub>	l <sub>dmaxi</sub>

FIG. 11



8/25

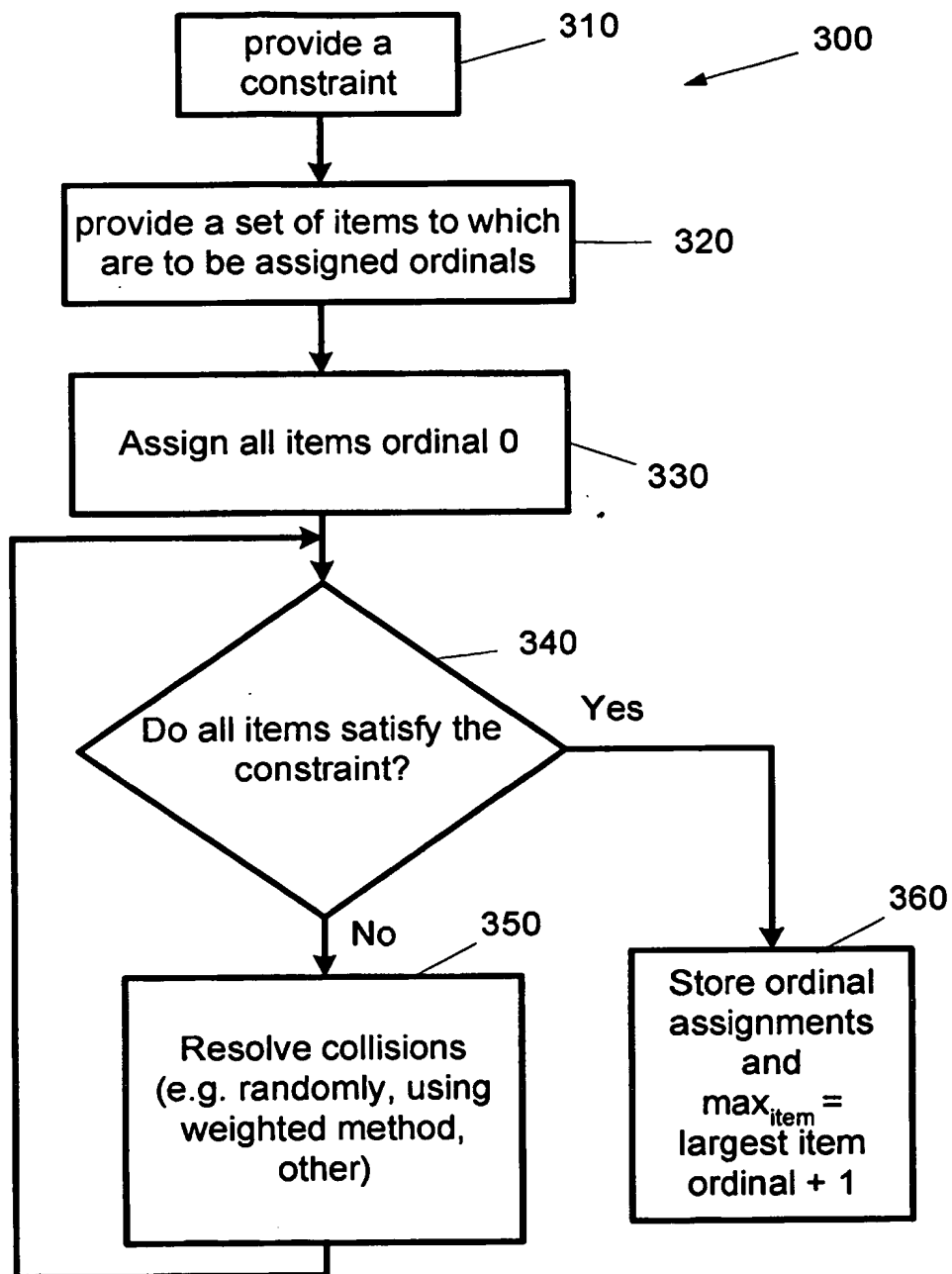


FIG. 12

9/25

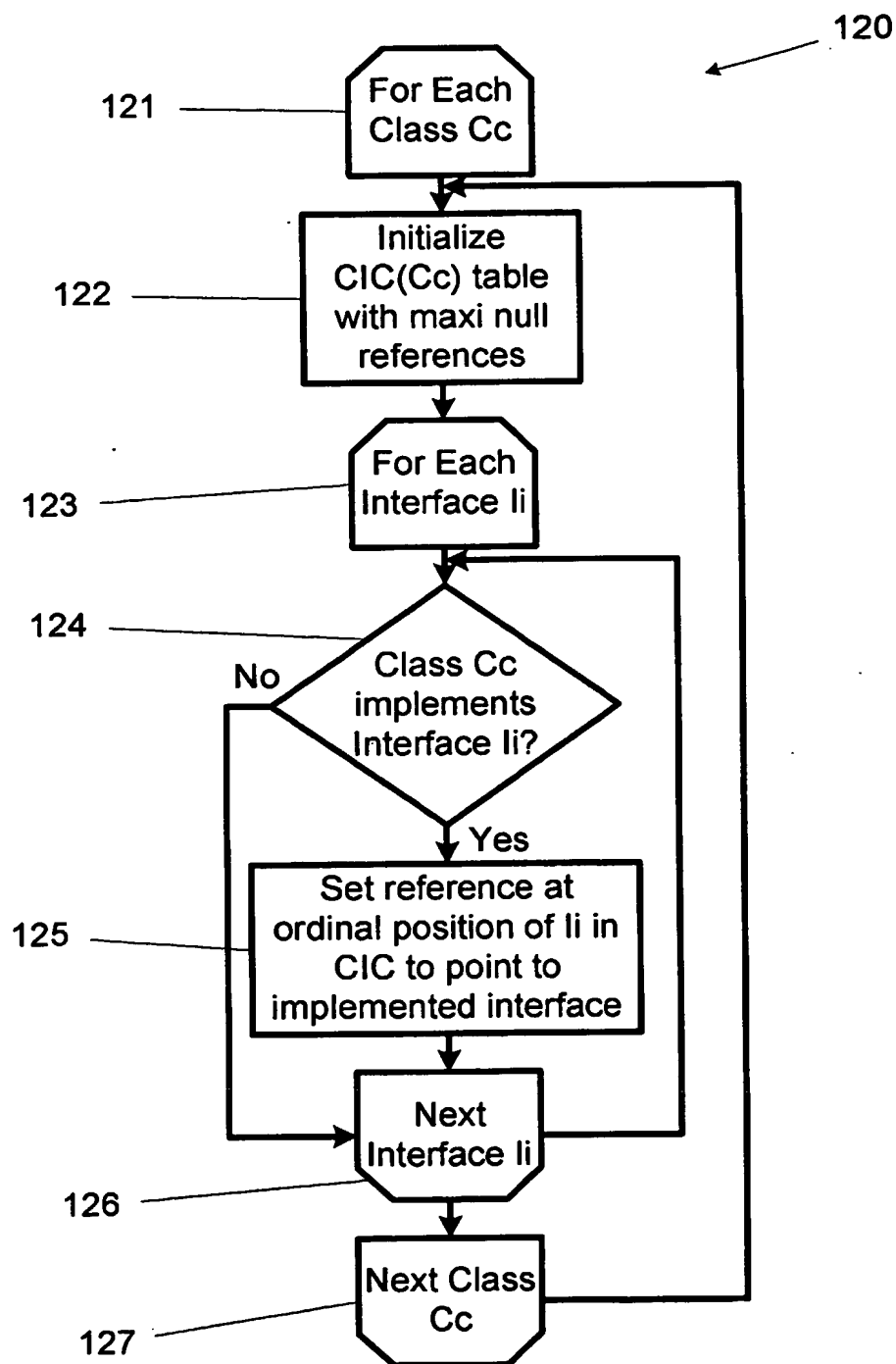


FIG. 13

10/25

128  
↙

CIC(Cr)	
ordinal <sub>i</sub>	@
0	null
1	null
...	...
ordinal <sub>i</sub> (I <sub>x</sub> )	@I <sub>x</sub>
...	...
ordinal <sub>i</sub> (I <sub>y</sub> )	@I <sub>y</sub>
...	...
max <sub>i</sub>	null

FIG. 14

11/25

SECOND CONSTRAINT

$$\left\{ \begin{array}{l} \forall C_r, I_x, I_y, m_w \in I_x, m_z \in I_y | \\ (C_r \text{ implements } I_x \wedge C_r \text{ implements } I_y) \\ \Rightarrow \\ (\text{ordinal}_m(m_w) \diamond \text{ordinal}_m(m_z)) \\ \wedge \\ (\text{ordinal}_m(m_w), \text{ordinal}_m(m_z) \in \{0, 1, \dots, \max_{mi}\}) \end{array} \right\}$$

135

FIG. 15

137

Method ordinals	
ordinal <sub>m</sub>	Method signatures
0	{m <sub>a0</sub> , m <sub>b0</sub> , m <sub>c0</sub> , ..., m <sub>d0</sub> }
1	{m <sub>a1</sub> , m <sub>b1</sub> , m <sub>c1</sub> , ..., m <sub>d1</sub> }
...	...
ordinal <sub>m</sub> (m <sub>w</sub> )	m <sub>w</sub>
...	...
ordinal <sub>m</sub> (m <sub>z</sub> )	m <sub>z</sub>
...	...
max <sub>m</sub>	m <sub>d max m</sub>

FIG. 16

12/25

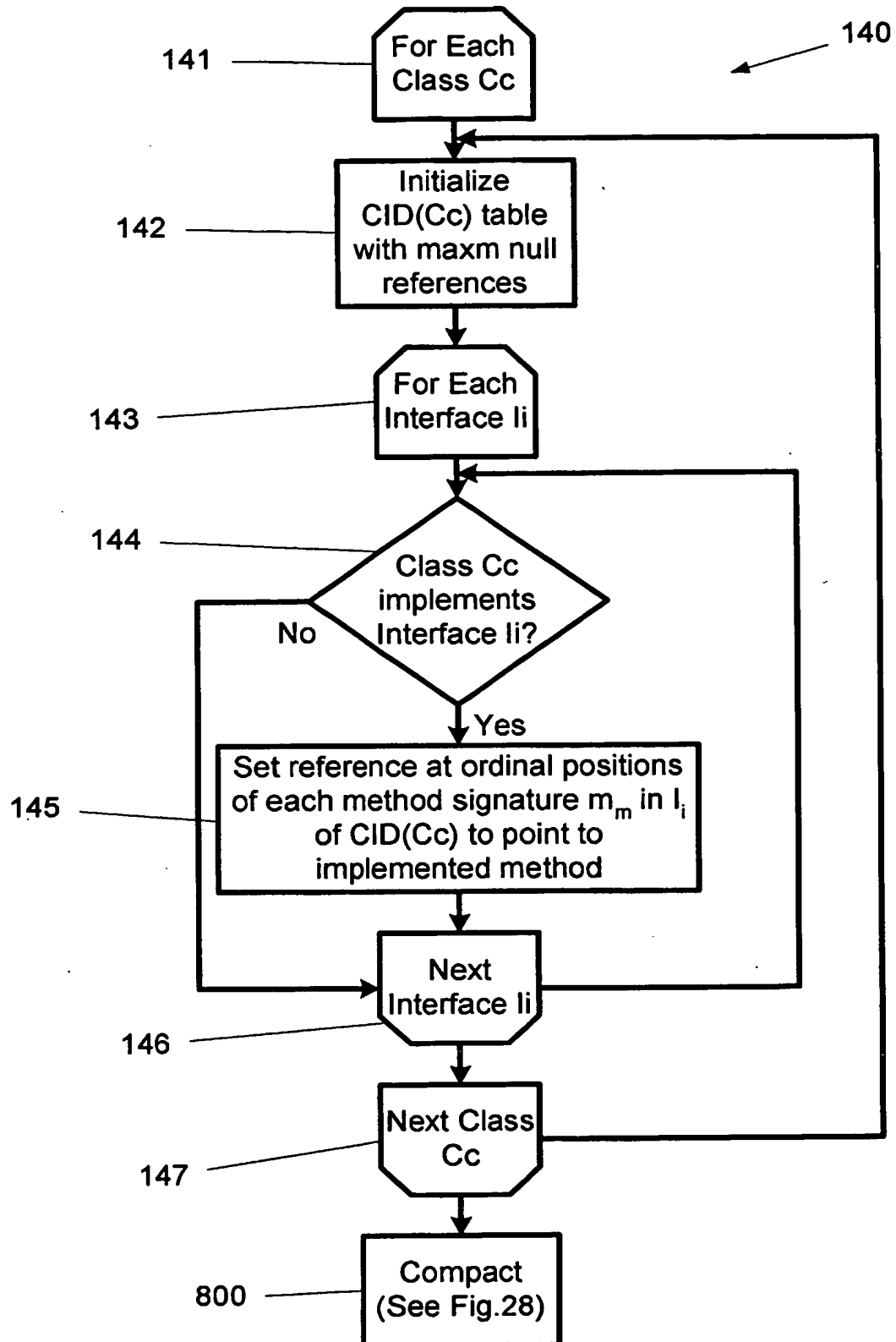
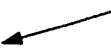


FIG. 17

13/25

148



CID(Cr)	
$\text{ordinal}_i$	@
0	null
1	null
...	...
$\text{ordinal}_m(m_z)$	@m <sub>z</sub>
...	...
$\text{ordinal}_m(m_w)$	@m <sub>w</sub>
...	...
max <sub>mi</sub>	null

FIG. 18

14/25

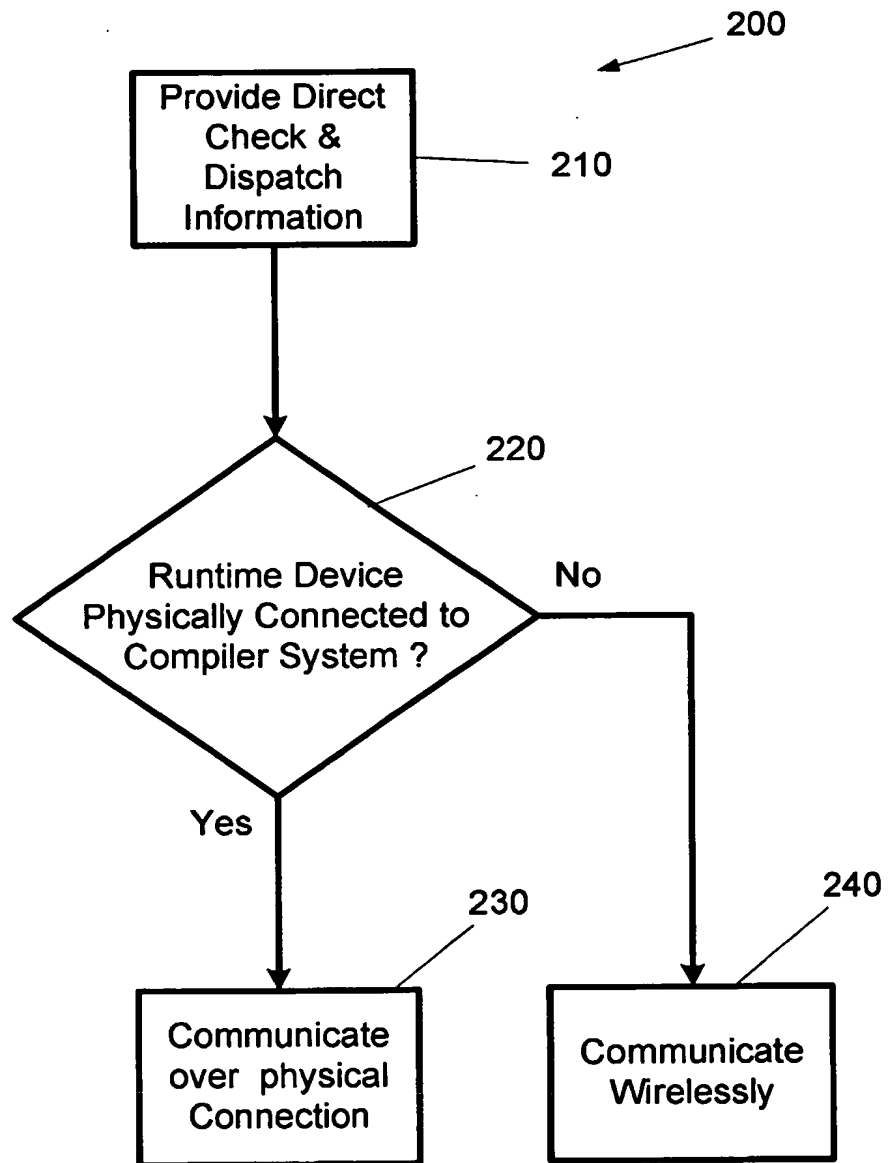


FIG. 19

15/25

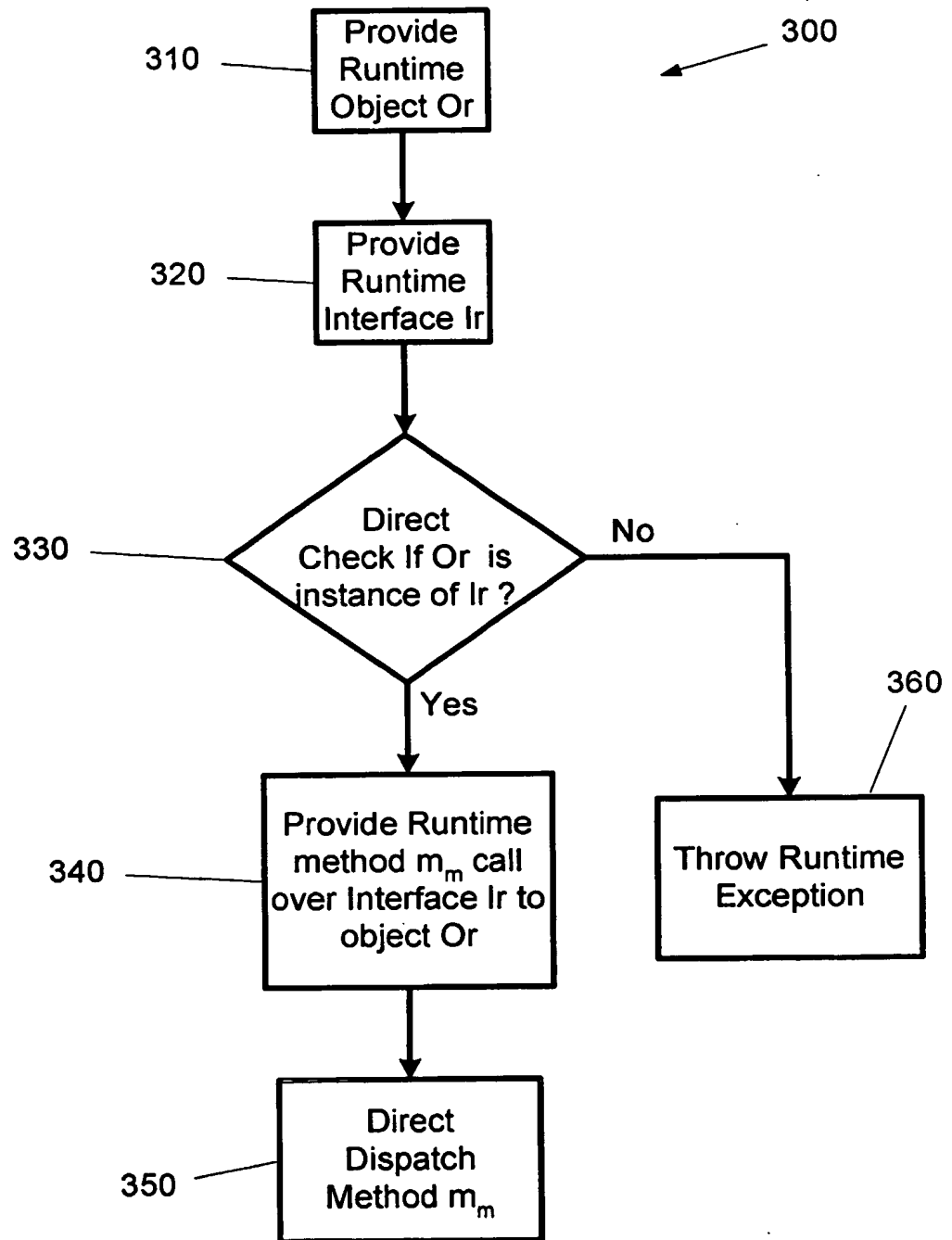


FIG. 20



16/25

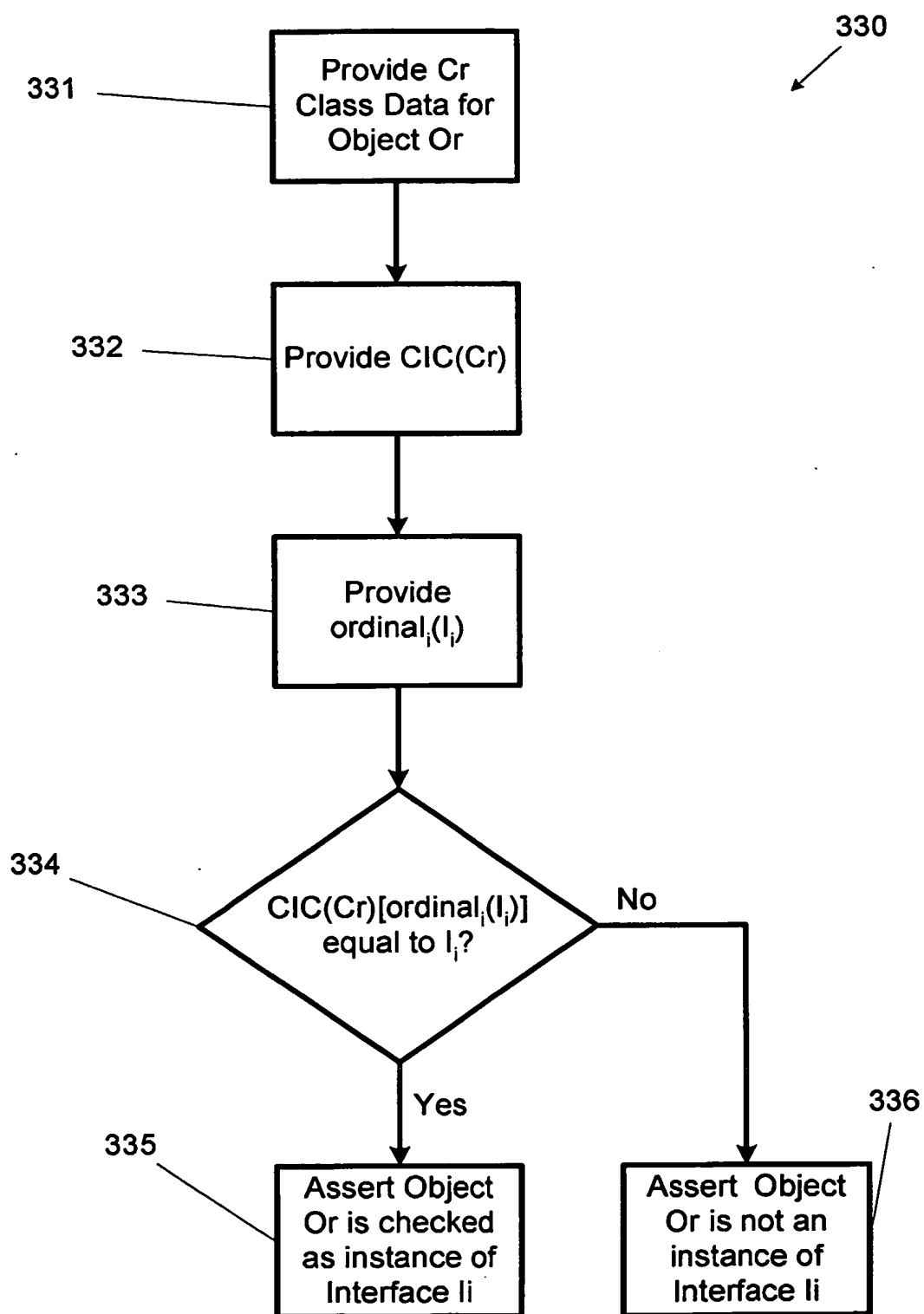


FIG. 21

17/25

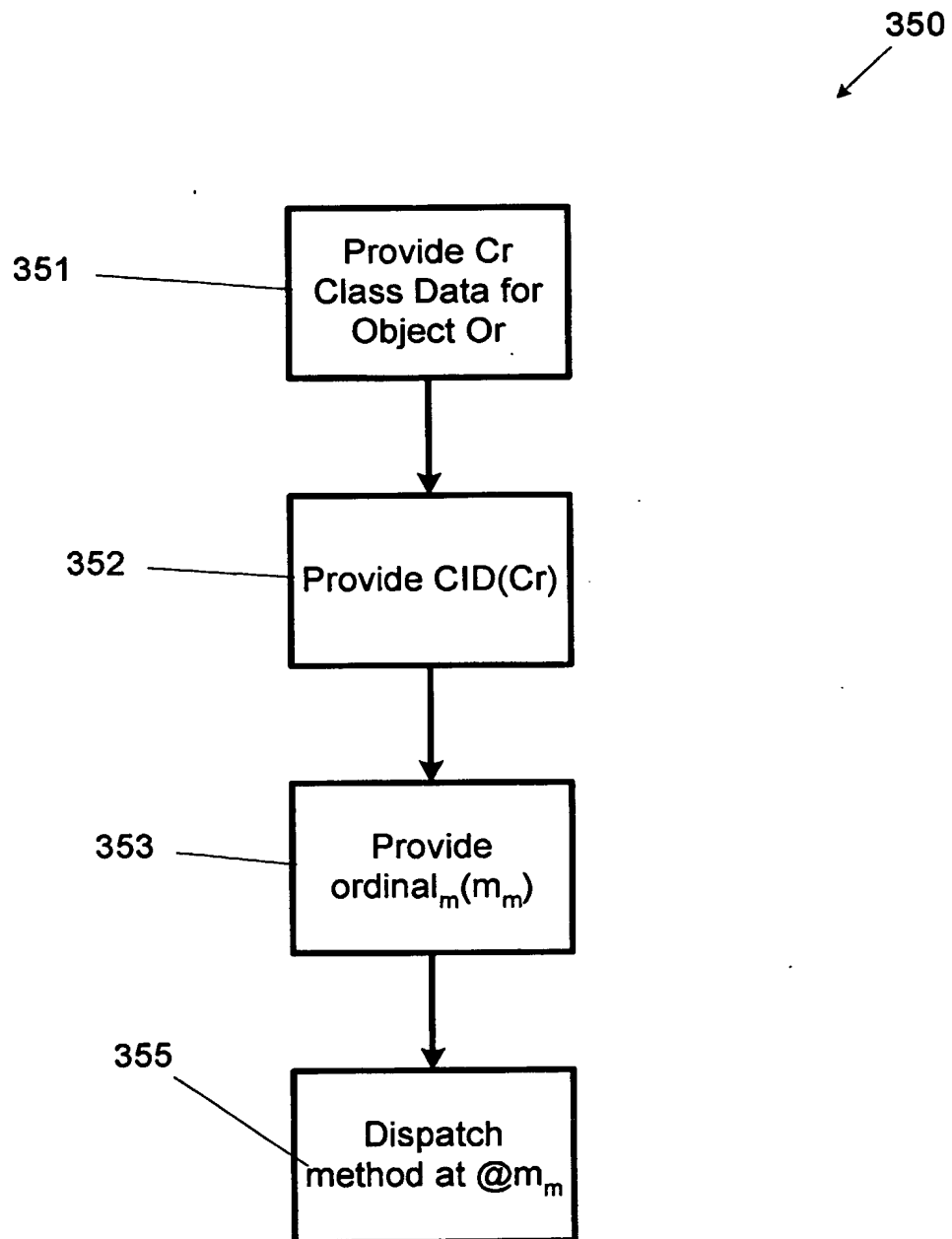


FIG. 22

18/25

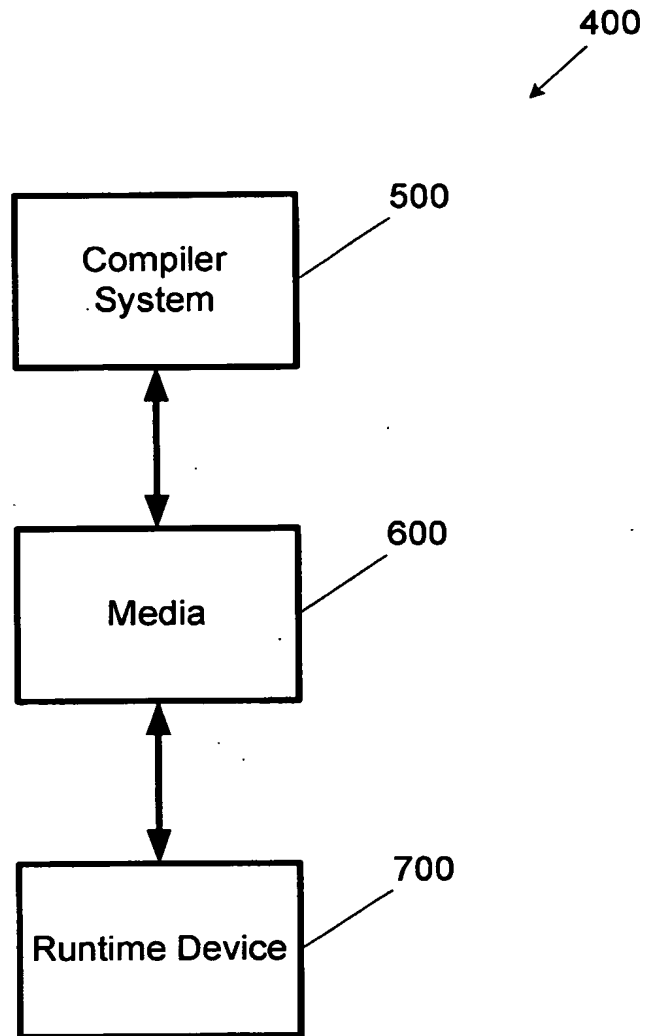


FIG. 23

19/25

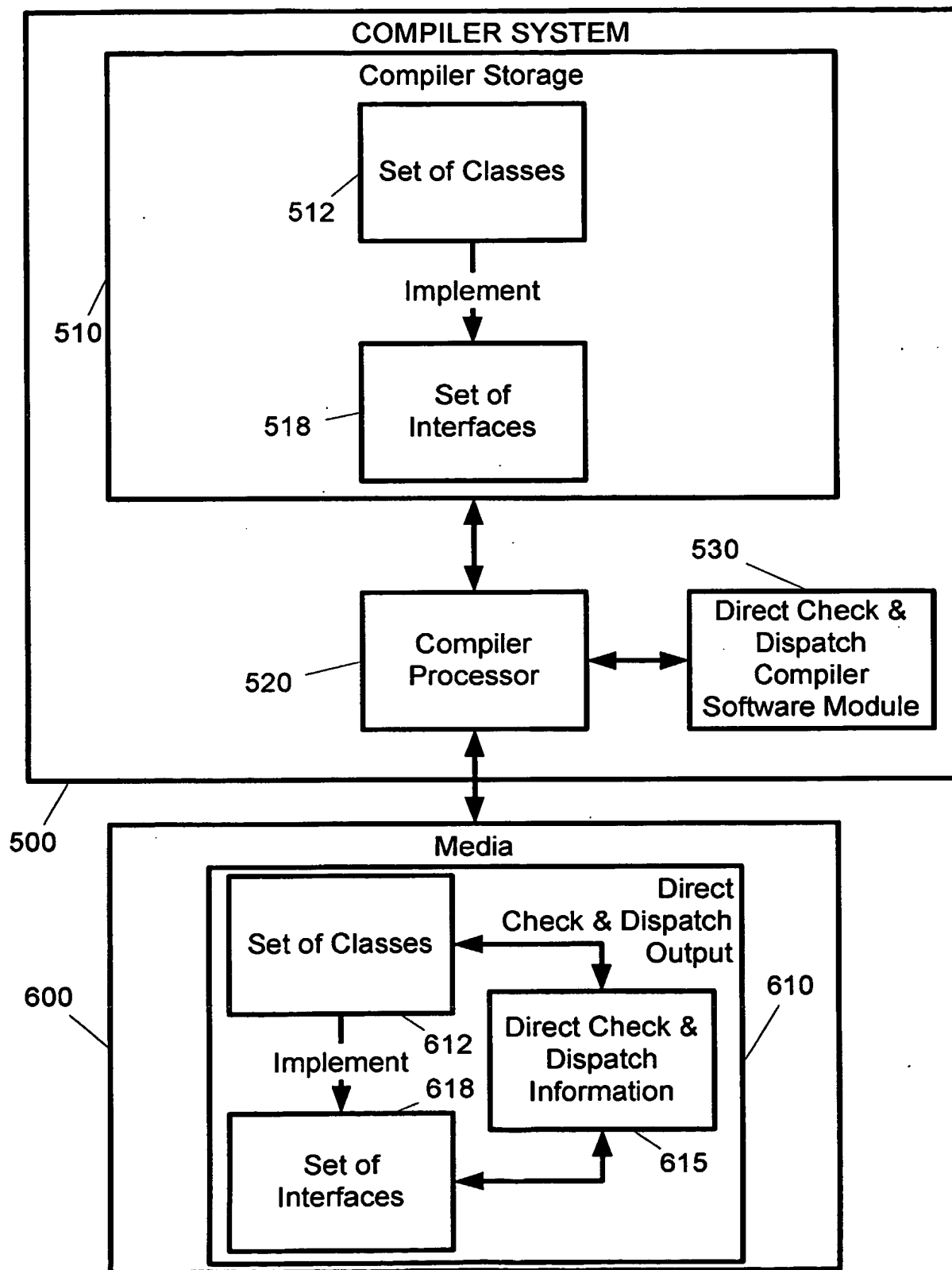


FIG. 24

20/25

MEDIA

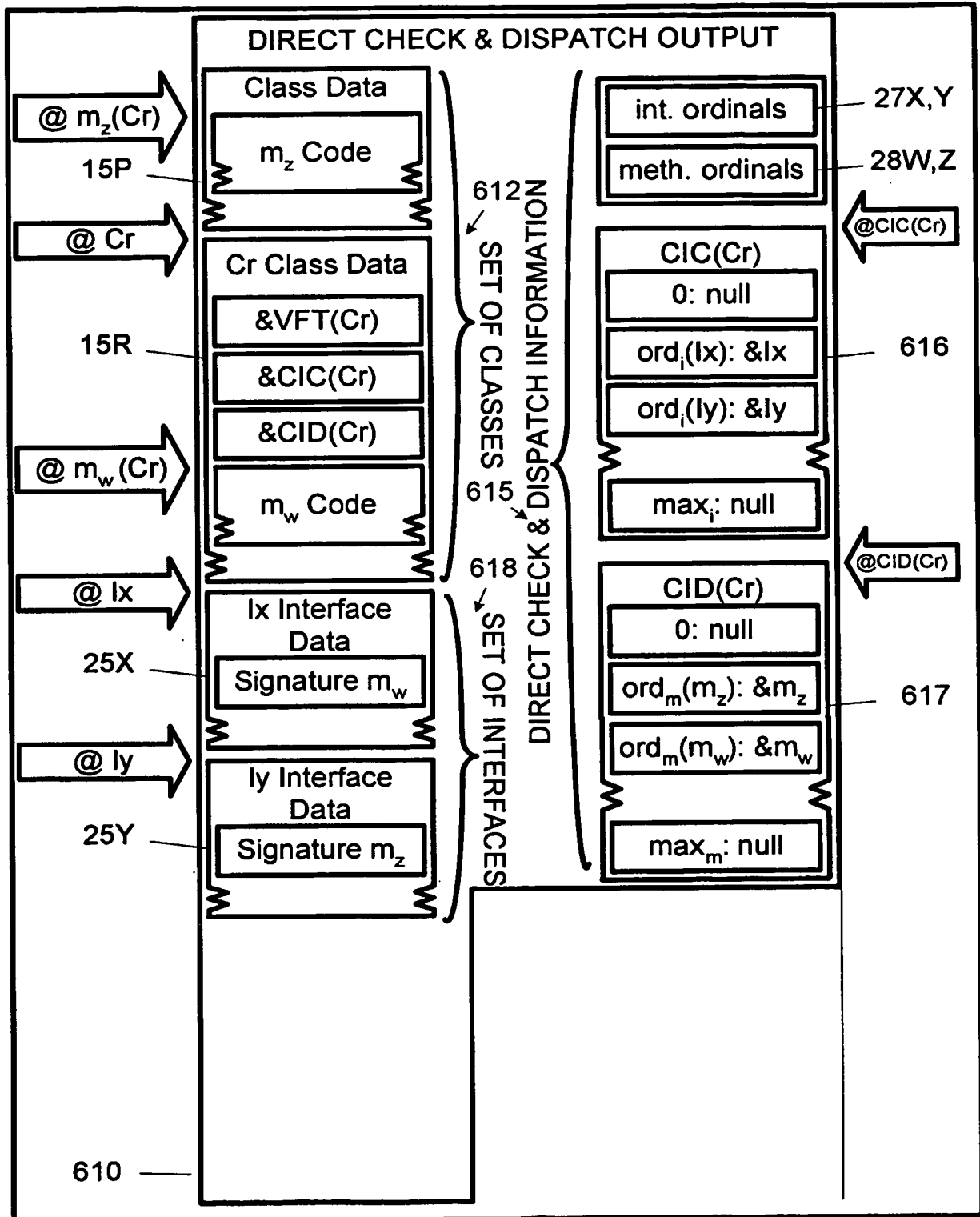


FIG. 25

21/25

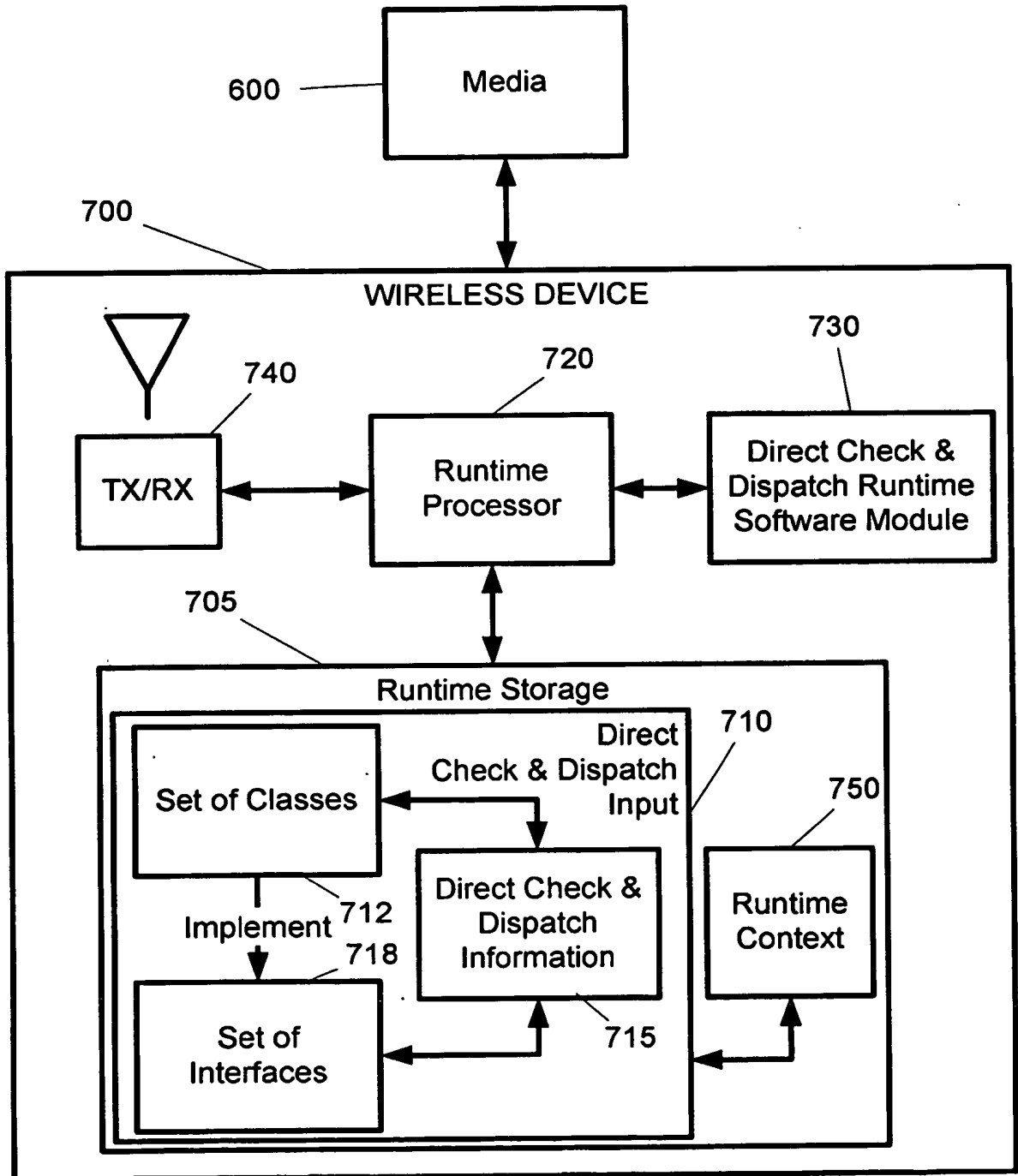
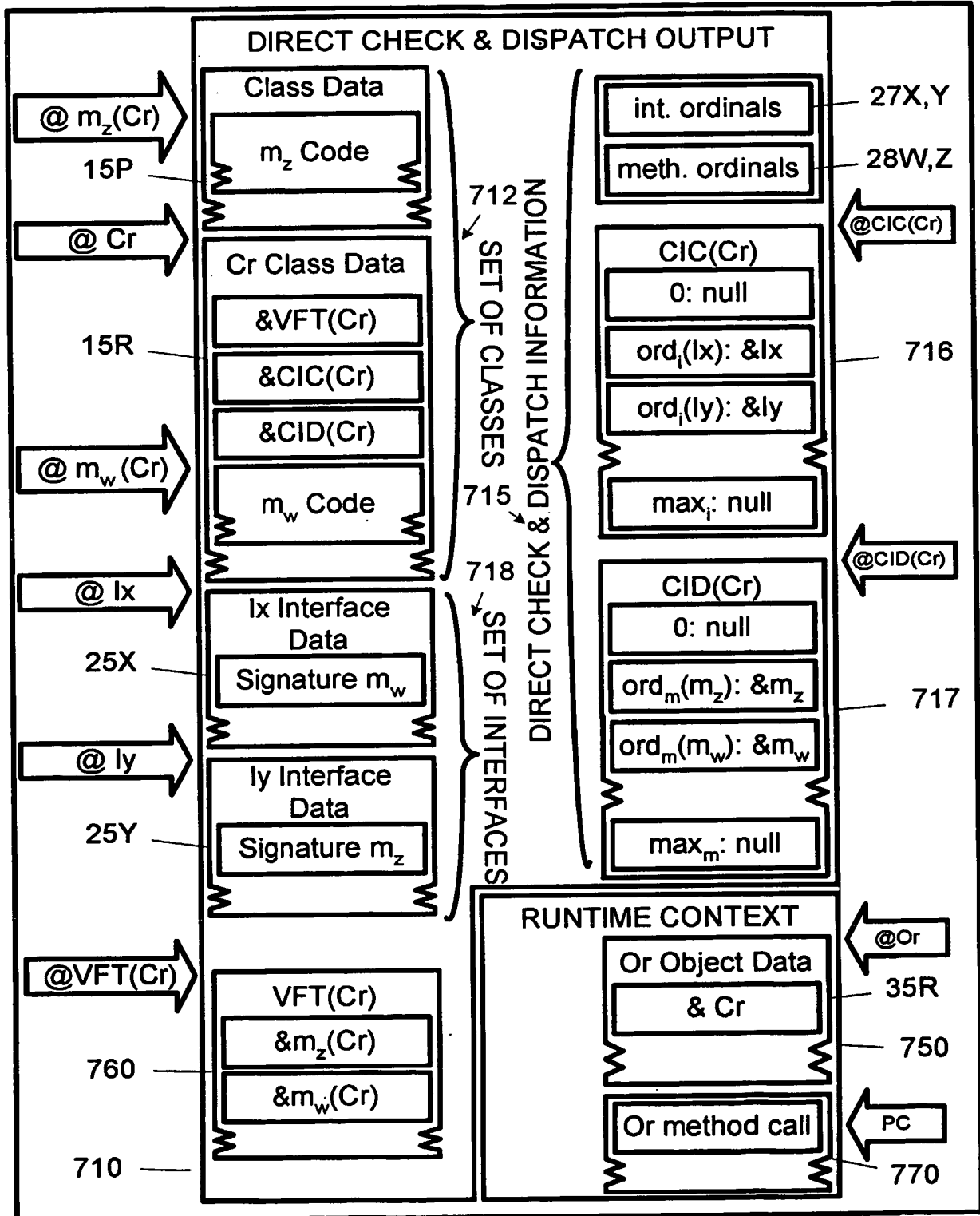


FIG. 26

22/25

## RUNTIME STORAGE



705

FIG. 27

23/25

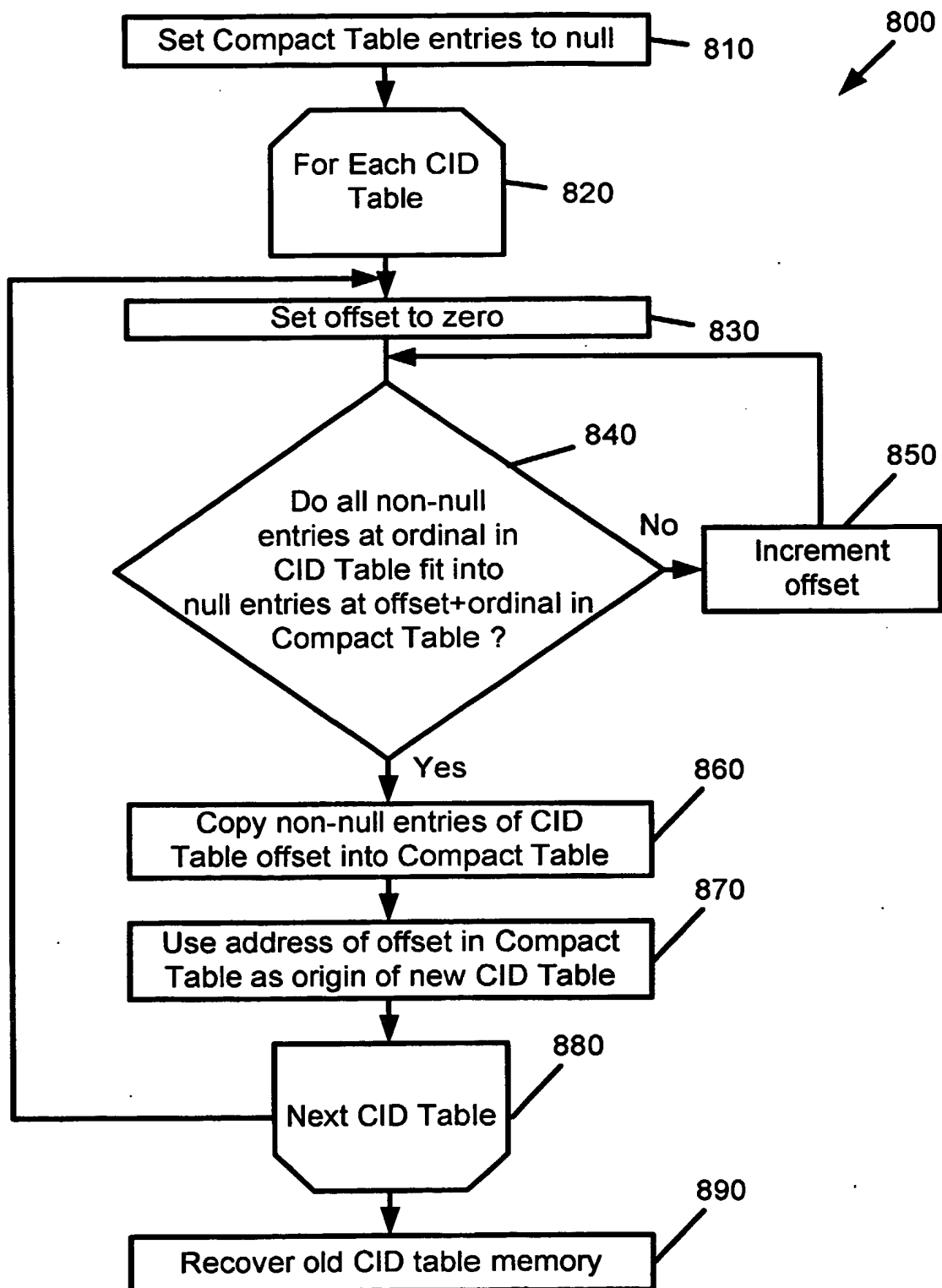


FIG. 28



24/25

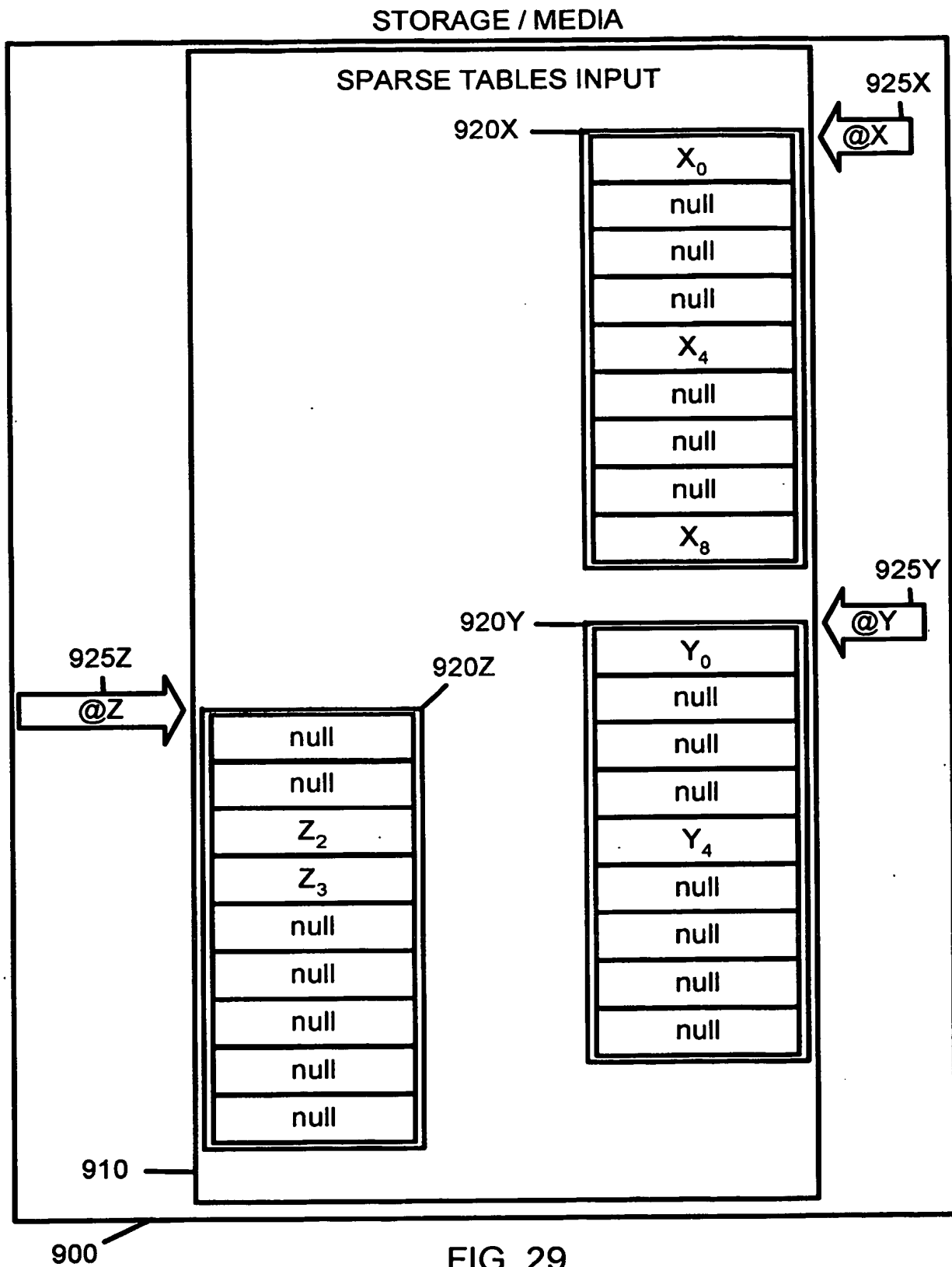


FIG. 29

25/25

STORAGE / MEDIA

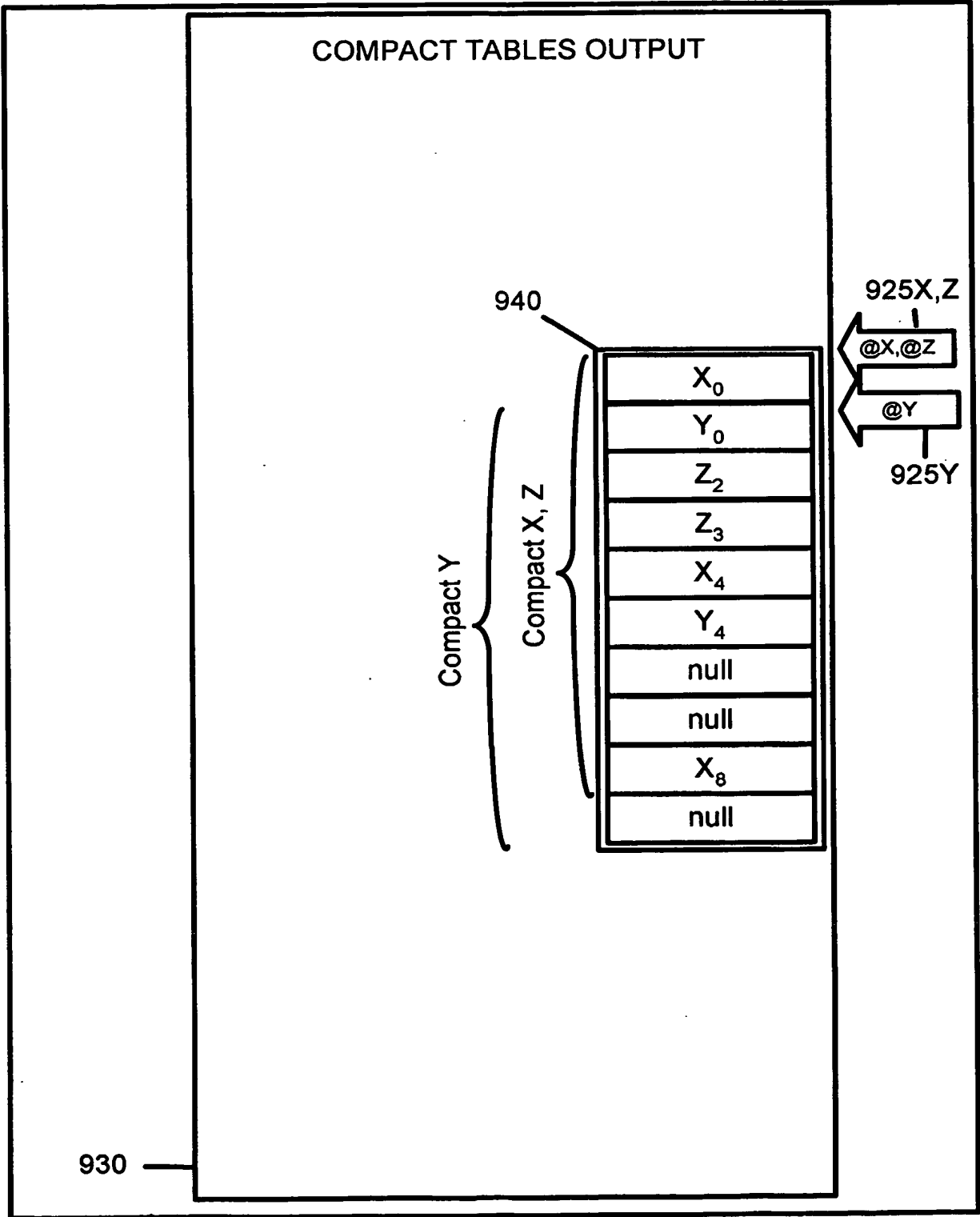


FIG. 30

(19) World Intellectual Property  
Organization  
International Bureau



(43) International Publication Date  
9 October 2003 (09.10.2003)

PCT

(10) International Publication Number  
**WO 2003/083653 A3**

(51) International Patent Classification<sup>7</sup>: **G06F 9/44**

F. [CA/CA]; 402 Cavendish Drive, Waterloo, Ontario N2T 2N6 (CA). YACH, David, P. [CA/CA]; 254 Castlefield Avenue, Waterloo, Ontario N2K 2N1 (CA).

(21) International Application Number:  
PCT/CA2003/000453

(22) International Filing Date: 28 March 2003 (28.03.2003)

(74) Agents: PATHIYAL, Krishna, K. et al.; Research In Motion Limited, 295 Phillip Street, Waterloo, Ontario N2L 3W8 (CA).

(25) Filing Language: English

(81) Designated States (*national*): AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NO, NZ, OM, PH, PL, PT, RO, RU, SC, SD, SE, SG, SK, SL, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, YU, ZA, ZM, ZW.

(26) Publication Language: English

(30) Priority Data:  
60/368,138 29 March 2002 (29.03.2002) US

(71) Applicant (*for all designated States except US*): RE-SEARCH IN MOTION LIMITED [CA/CA]; 295 Phillip Street, Waterloo, Ontario N2L 3W8 (CA).

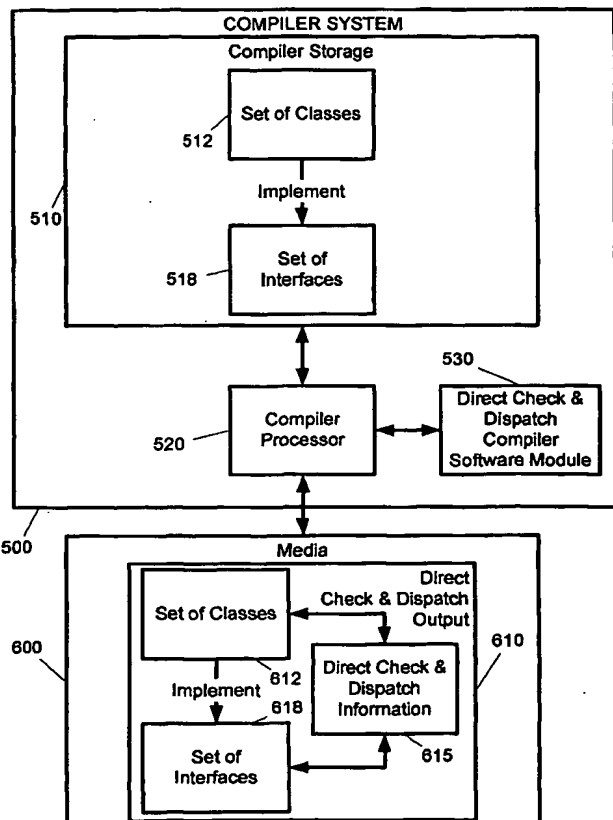
(84) Designated States (*regional*): ARIPO patent (GH, GM, KE, LS, MW, MZ, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, BG, CH, CY, CZ, DE, DK, EE,

(72) Inventors; and

(75) Inventors/Applicants (*for US only*): SCIAN, Anthony,

[Continued on next page]

(54) Title: SYSTEM AND METHOD FOR OBJECT ORIENTED INTERFACE CHECKING AND METHOD DISPATCHING



(57) Abstract: Method and system for interface checking and interface method dispatching for wireless devices. Relationships between classes and objectoriented interfaces are analyzed and certain properties are exploited for use in performing interface checking and/or interface method dispatching.



ES, FI, FR, GB, GR, HU, IE, IT, LU, MC, NL, PT, RO, SE, SI, SK, TR), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

#### Declarations under Rule 4.17:

- *as to applicant's entitlement to apply for and be granted a patent (Rule 4.17(ii)) for the following designations* AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NO, NZ, OM, PH, PL, PT, RO, RU, SC, SD, SE, SG, SK, SL, TJ, TM, TN, TR, TT, TZ, UA, UG, UZ, VC, VN, YU, ZA, ZM, ZW, ARIPO patent (GH, GM, KE, LS, MW, MZ, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HU, IE, IT, LU, MC, NL, PT, RO, SE, SI, SK, TR), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG)
- *as to the applicant's entitlement to claim the priority of the earlier application (Rule 4.17(iii)) for the following designations* AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS,

JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NO, NZ, OM, PH, PL, PT, RO, RU, SC, SD, SE, SG, SK, SL, TJ, TM, TN, TR, TT, TZ, UA, UG, UZ, VC, VN, YU, ZA, ZM, ZW, ARIPO patent (GH, GM, KE, LS, MW, MZ, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HU, IE, IT, LU, MC, NL, PT, RO, SE, SI, SK, TR), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG)

- *of inventorship (Rule 4.17(iv)) for US only*

#### Published:

- *with international search report*
- *before the expiration of the time limit for amending the claims and to be republished in the event of receipt of amendments*

(88) Date of publication of the international search report:

4 March 2004

*For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.*

## INTERNATIONAL SEARCH REPORT

International Application No

PCT/CA 03/00453

**A. CLASSIFICATION OF SUBJECT MATTER**  
IPC 7 G06F9/44

According to International Patent Classification (IPC) or to both national classification and IPC

**B. FIELDS SEARCHED**

Minimum documentation searched (classification system followed by classification symbols)

IPC 7 G06F H04L

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal, WPI Data, PAJ, IBM-TDB, INSPEC

**C. DOCUMENTS CONSIDERED TO BE RELEVANT**

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
Y	ROSE J R: "FAST DISPATCH MECHANISMS FOR STOCK HARDWARE" PROCEEDINGS OF THE OBJECT ORIENTED PROGRAMMING SYSTEMS LANGUAGES AND APPLICATIONS CONFERENCE. (OOPSLA). SAN DIEGO, SEPT. 25 - 30, 1988. SPECIAL ISSUE OF SIGPLAN NOTICES, VOL. 23, NO. 11, NOV. 1988, READING, ACM, US, vol. CONF. 3, 25 September 1988 (1988-09-25), pages 27-35, XP000299814 the whole document ---	1-10
Y	US 5 889 995 A (SEGNAN MARINO) 30 March 1999 (1999-03-30) the whole document ---	1-10
-/--		

☒ Further documents are listed in the continuation of box C.☒ Patent family members are listed in annex.

## \* Special categories of cited documents:

- \*A\* document defining the general state of the art which is not considered to be of particular relevance
- \*E\* earlier document but published on or after the international filing date
- \*L\* document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)
- \*O\* document referring to an oral disclosure, use, exhibition or other means
- \*P\* document published prior to the international filing date but later than the priority date claimed

- \*T\* later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention
- \*X\* document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone
- \*Y\* document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art.
- \*G\* document member of the same patent family

Date of the actual completion of the international search

28 November 2003

Date of mailing of the international search report

07/01/2004

Name and mailing address of the ISA

European Patent Office, P.B. 5818 Patentlaan 2  
NL - 2280 HV Rijswijk  
Tel. (+31-70) 340-2040, Tx. 31 651 epo nl,  
Fax: (+31-70) 340-3016

Authorized officer

Peper, J

# INTERNATIONAL SEARCH REPORT

International Application No

PCT/CA 03/00453

## C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	<p>WO 00 77617 A (SUN MICROSYSTEMS INC)  21 December 2000 (2000-12-21)  page 1, line 1 -page 4, line 26; figures  1-4</p> <p style="text-align: center;">---</p>	1-10
A	<p>KAY ROEMER: "MICO - MICO IS CORBA. Eine  erweiterbare corba-Implementierung für  Forschung und Ausbildung"  PH D THESIS,  February 1998 (1998-02), XP002157372  frankfurt am Main  page I -page III; figures 5.10,5.11  paragraph '2.2.2!  paragraph '2.5.1! - paragraph '2.5.2!  paragraph '5.2.2!  paragraph '5.4.4!  paragraph '06.1! - paragraph '06.2!  paragraph '06.4!</p> <p style="text-align: center;">---</p>	1-3,7-9
A	<p>BILL VENNERS: "Chapter 8 - The Linking  Model"  INSIDE THE JAVA VIRTUAL MACHINE, 'Online!  6 January 2000 (2000-01-06), XP002259055  McGraw-Hill  ISBN: 0-07-135093-4  Retrieved from the Internet:  &lt;URL:http://www.artima.com/insidejvm/ed2/1  inkmod.html&gt; 'retrieved on 2003-10-24!  the whole document</p> <p style="text-align: center;">-----</p>	1-10

## INTERNATIONAL SEARCH REPORT

Information on patent family members

International Application No

PCT/CA 03/00453

Patent document cited in search report		Publication date	Patent family member(s)	Publication date
US 5889995	A	30-03-1999	NONE	
WO 0077617	A	21-12-2000	AU 5733500 A EP 1190307 A2 WO 0077617 A2	02-01-2001 27-03-2002 21-12-2000

**This Page is Inserted by IFW Indexing and Scanning  
Operations and is not part of the Official Record**

**BEST AVAILABLE IMAGES**

Defective images within this document are accurate representations of the original documents submitted by the applicant.

Defects in the images include but are not limited to the items checked:

- ☐ BLACK BORDERS
- ☐ IMAGE CUT OFF AT TOP, BOTTOM OR SIDES
- ☐ FADED TEXT OR DRAWING
- ☒ BLURRED OR ILLEGIBLE TEXT OR DRAWING
- ☐ SKEWED/SLANTED IMAGES
- ☒ COLOR OR BLACK AND WHITE PHOTOGRAPHS
- ☐ GRAY SCALE DOCUMENTS
- ☐ LINES OR MARKS ON ORIGINAL DOCUMENT
- ☐ REFERENCE(S) OR EXHIBIT(S) SUBMITTED ARE POOR QUALITY
- ☐ OTHER: \_\_\_\_\_

**IMAGES ARE BEST AVAILABLE COPY.**

**As rescanning these documents will not correct the image problems checked, please do not report these problems to the IFW Image Problem Mailbox.**